

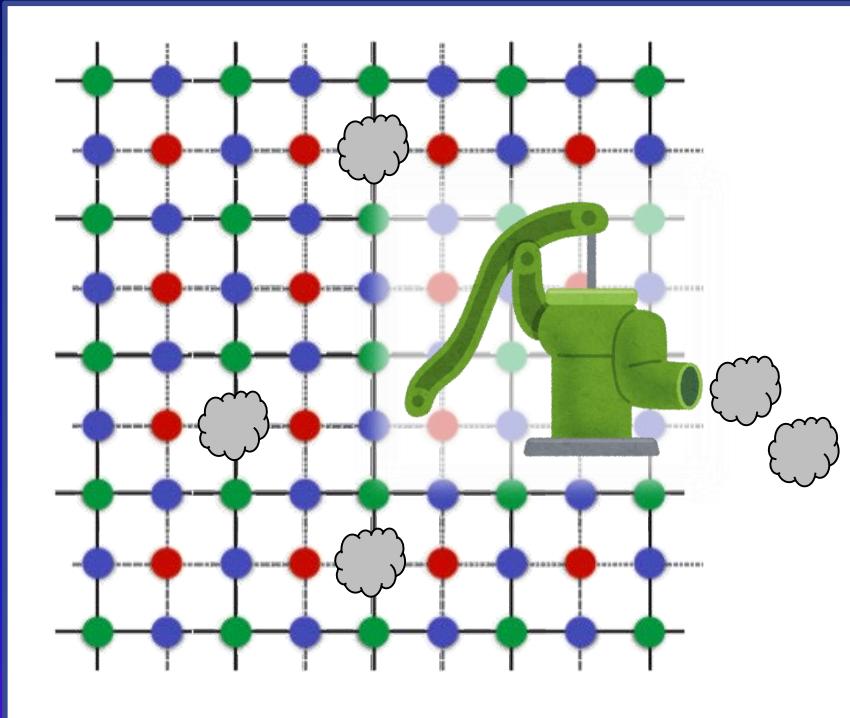
# Development of quantum hardware towards fault-tolerant quantum computing

Yasunobu Nakamura

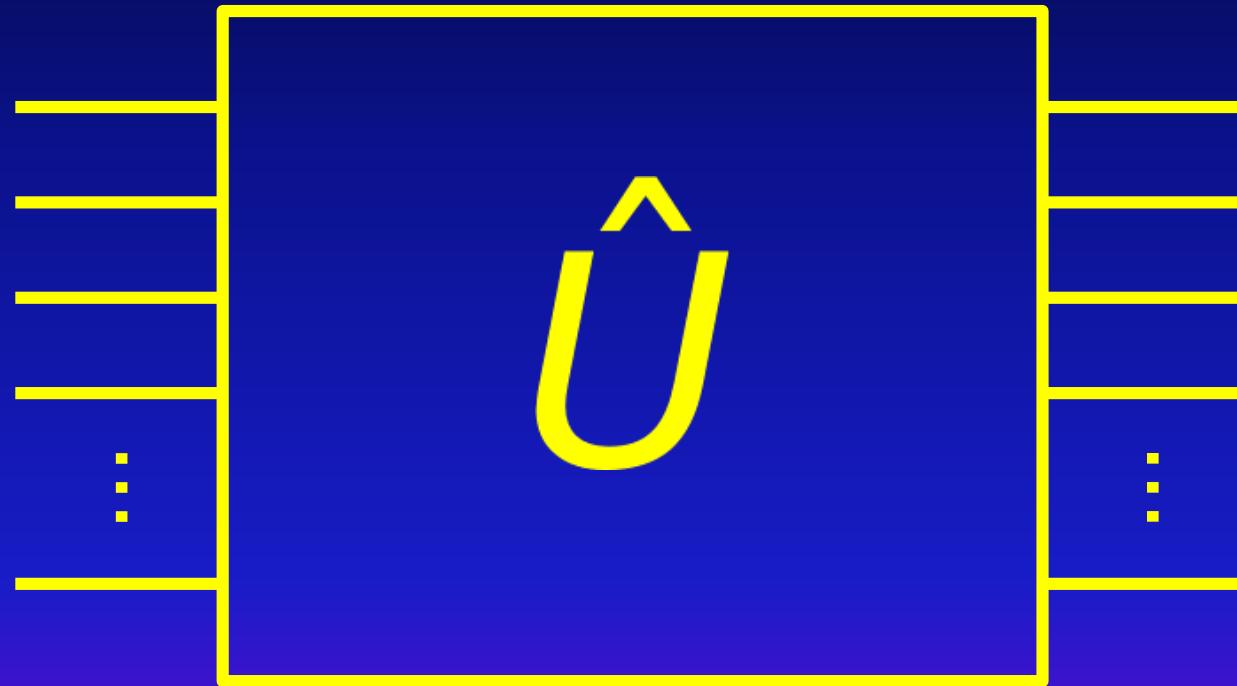
Research Center for Advanced Science and Technology  
(RCAST), The University of Tokyo

Center for Emergent Matter Science (CEMS), RIKEN

# Fault-tolerant quantum computer = “Perpetual” quantum machines

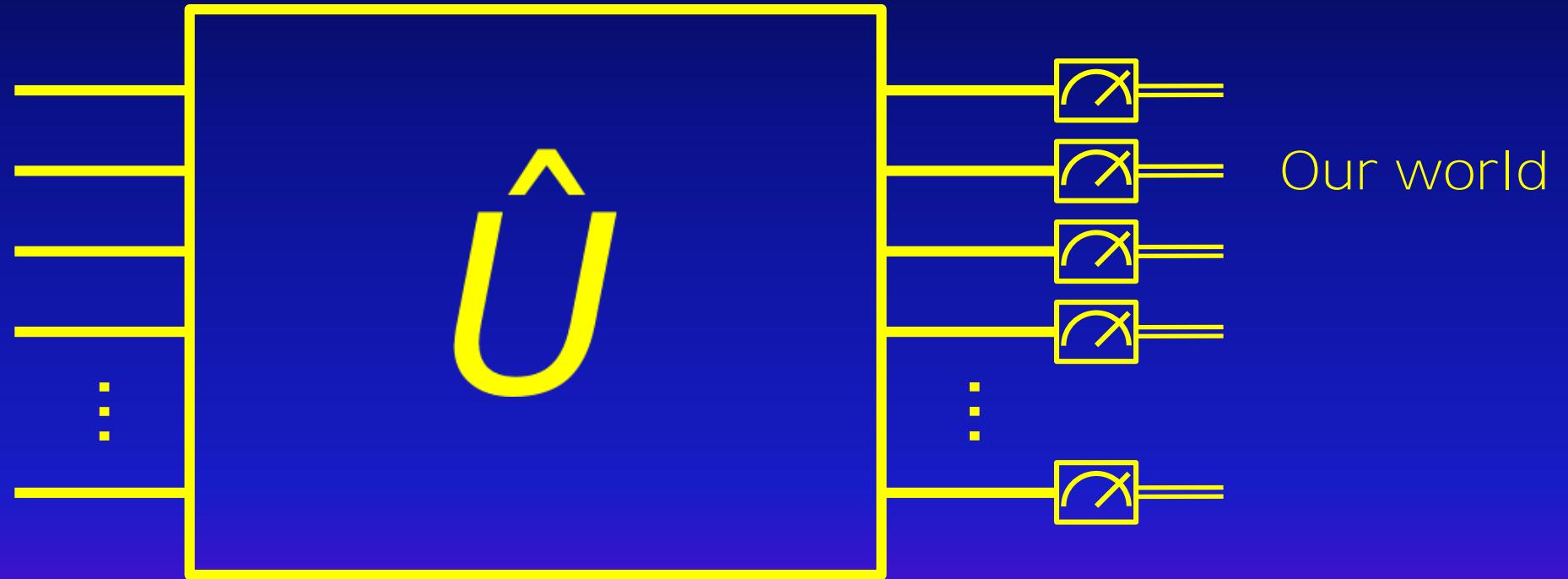


# Fault-tolerant quantum computer = “**Perpetual**” quantum machines



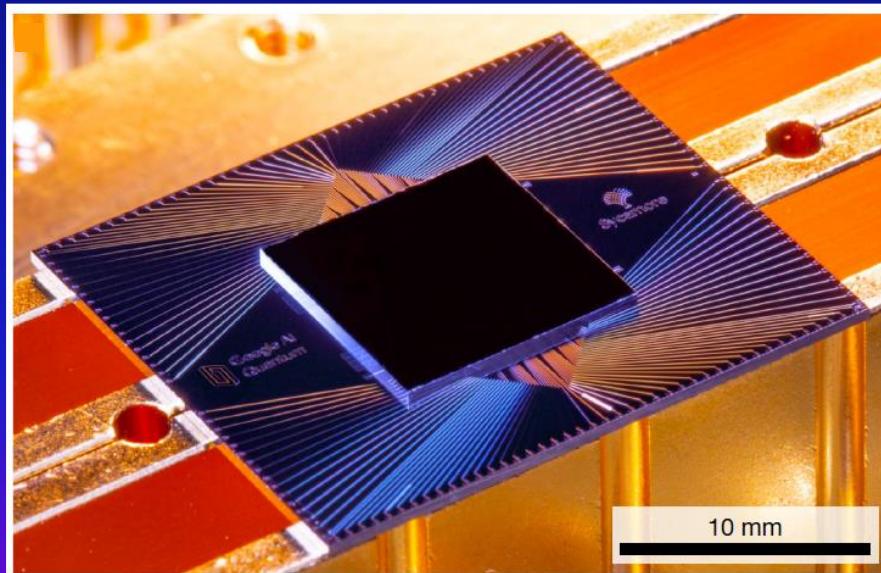
Many world !!!  
 $O(2^N)$

# Fault-tolerant quantum computer = “**Perpetual**” quantum machines



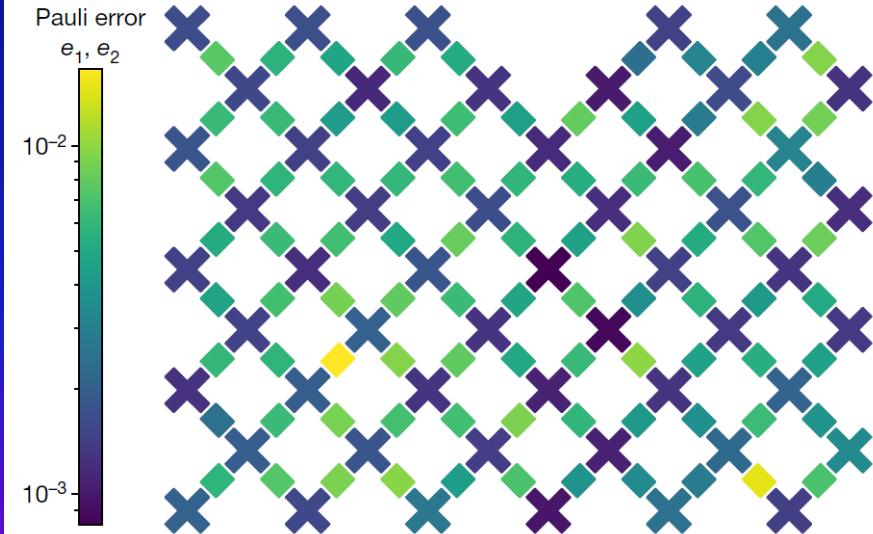
# Quantum supremacy using a programmable superconducting processor

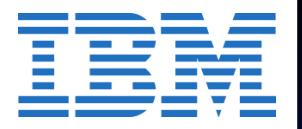
- Flip-chip bonding
- Frequency-tunable qubits
- Tunable-coupler architecture
  - Fast two-qubit gate  $\sim 12$  ns
  - Small residual coupling



Google AI Quantum  
Nature 574, 505 (2019)

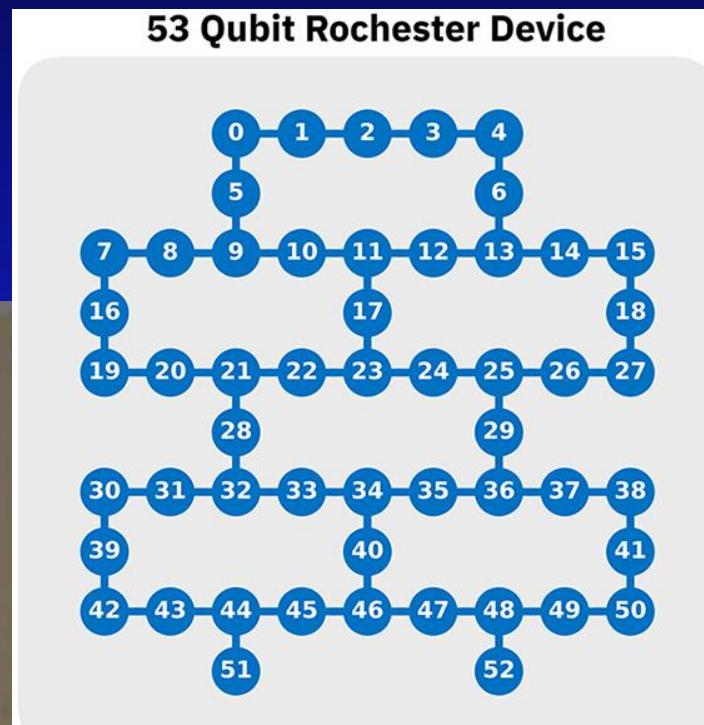
	Average error	Isolated	Simultaneous
Single-qubit ( $e_1$ )	0.15%	0.16%	
Two-qubit ( $e_2$ )	0.36%	0.62%	
Two-qubit, cycle ( $e_{2c}$ )	0.65%	0.93%	
Readout ( $e_r$ )	3.1%	3.8%	





# Fixed-frequency qubits

- Long coherence ~ 100  $\mu$ s
- Cross-resonance gate ~ 150 ns
- 16-bit GHZ state generation
- 20-bit, 53-bit cloud service



[https://www.ibm.com/blogs/research/2017/11/the-future-is-quantum/?utm\\_source=twitter&utm\\_medium=social&utm\\_campaign=ibmq&utm\\_content=2050qubit](https://www.ibm.com/blogs/research/2017/11/the-future-is-quantum/?utm_source=twitter&utm_medium=social&utm_campaign=ibmq&utm_content=2050qubit)

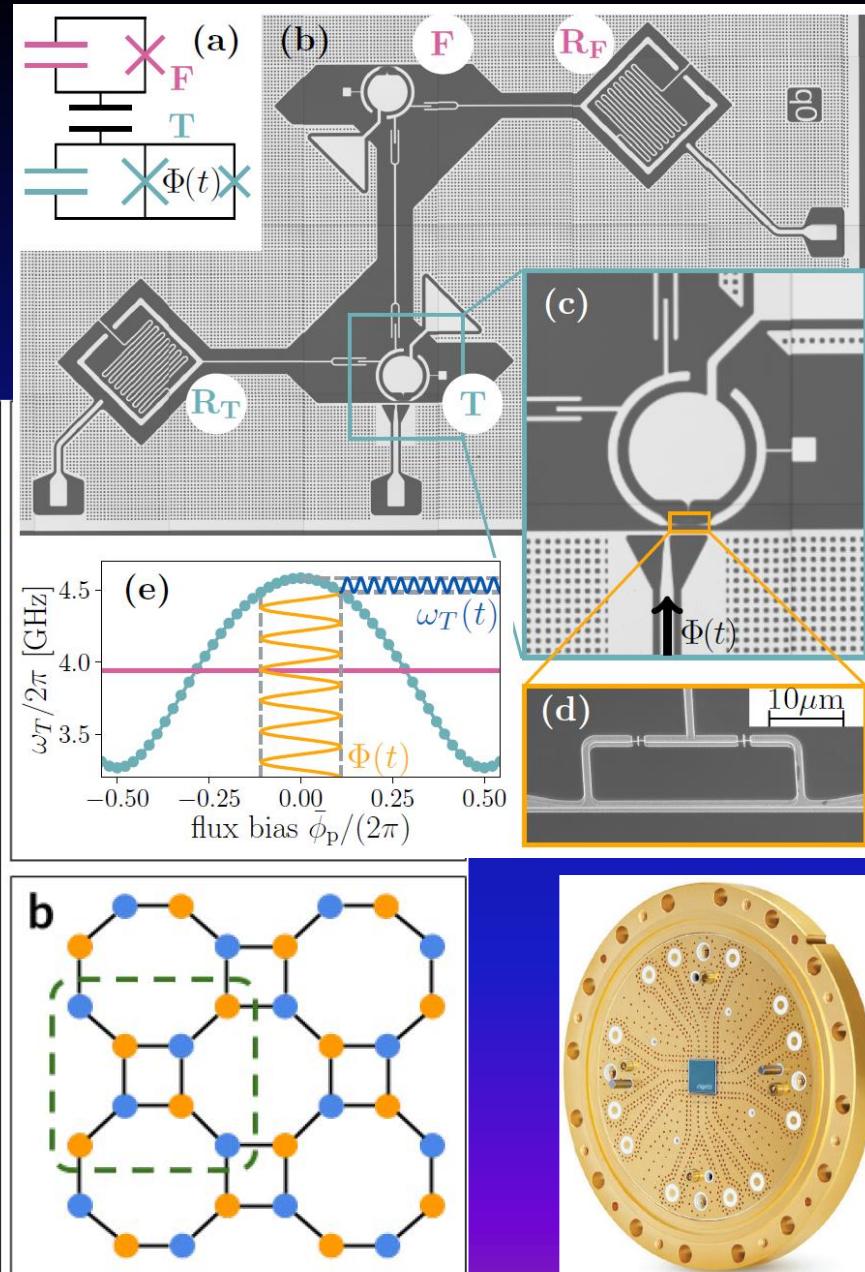
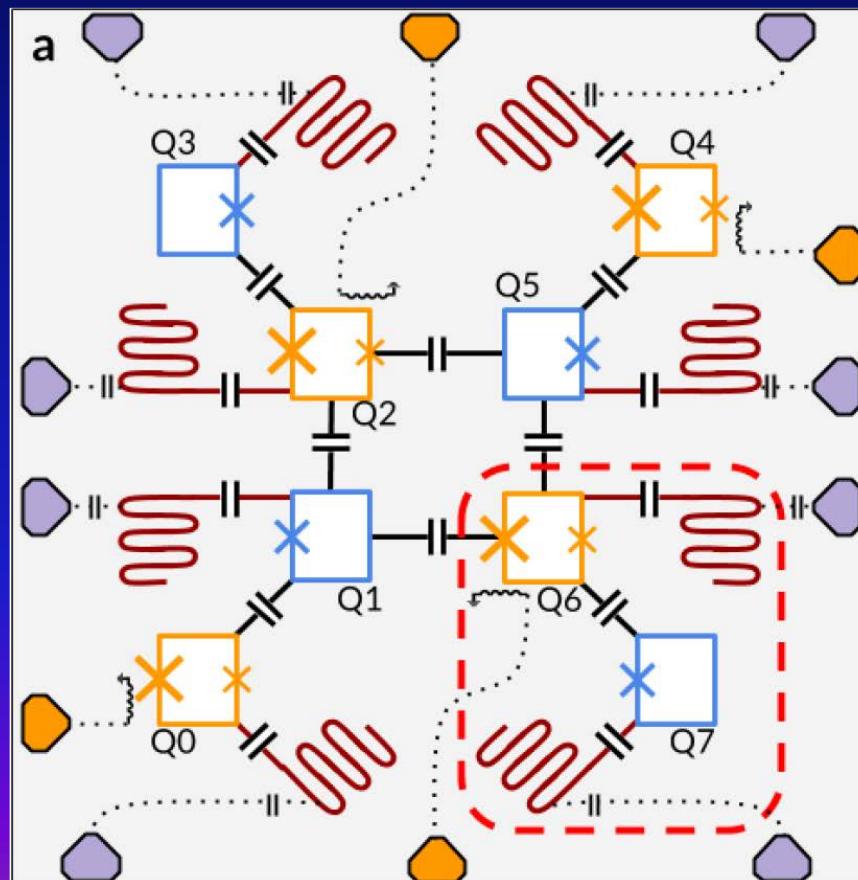
IBM Q System One (In preparation for the IBM Q Network)	
<b>Relaxation (T1) in microseconds</b>	73.9 <b>132.9</b> 38.2
mean	
best	
worst	
<b>Dephasing (T2) in microseconds</b>	69.1 <b>100.8</b> 39.2
mean	
best	
worst	
<b>Two-qubit (CNOT) error rates <math>\times 10^{-2}</math></b>	1.69 <b>0.97</b> 2.85
mean	
best	
worst	
<b>Single-qubit error rates <math>\times 10^{-3}</math></b>	0.41 <b>0.19</b> 0.82
mean	
best	
worst	

K.X. Wei et al. arXiv:1905.05720 (IBM)  
<https://www.ibm.com/blogs/research/2019/03/power-quantum-device/>  
<https://www.ibm.com/blogs/research/2019/09/quantum-computation-center/>

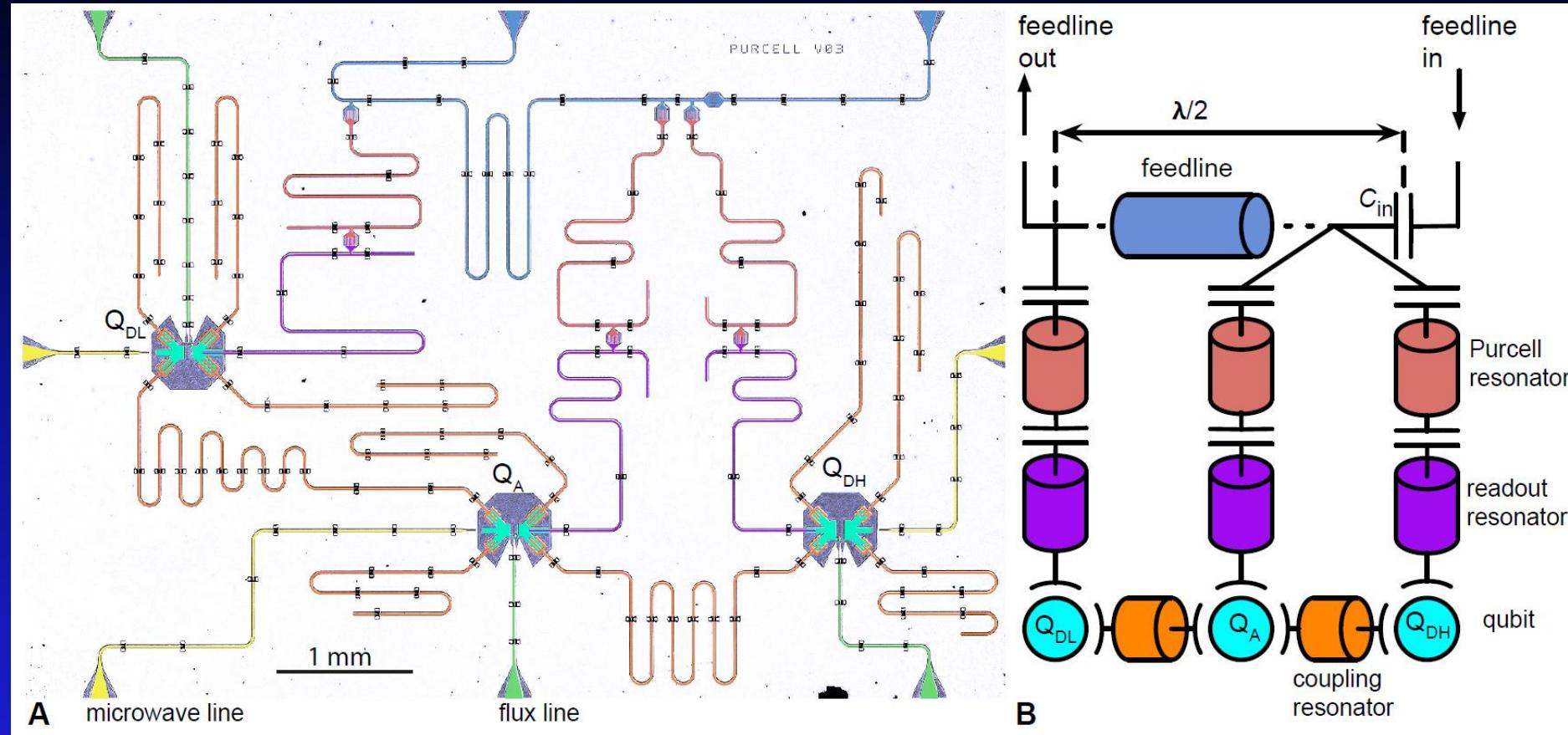
# Parametric coupling



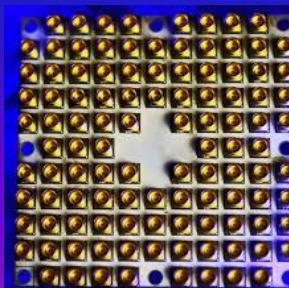
- Fixed- and tunable-freq qubits
- 16-bit cloud service



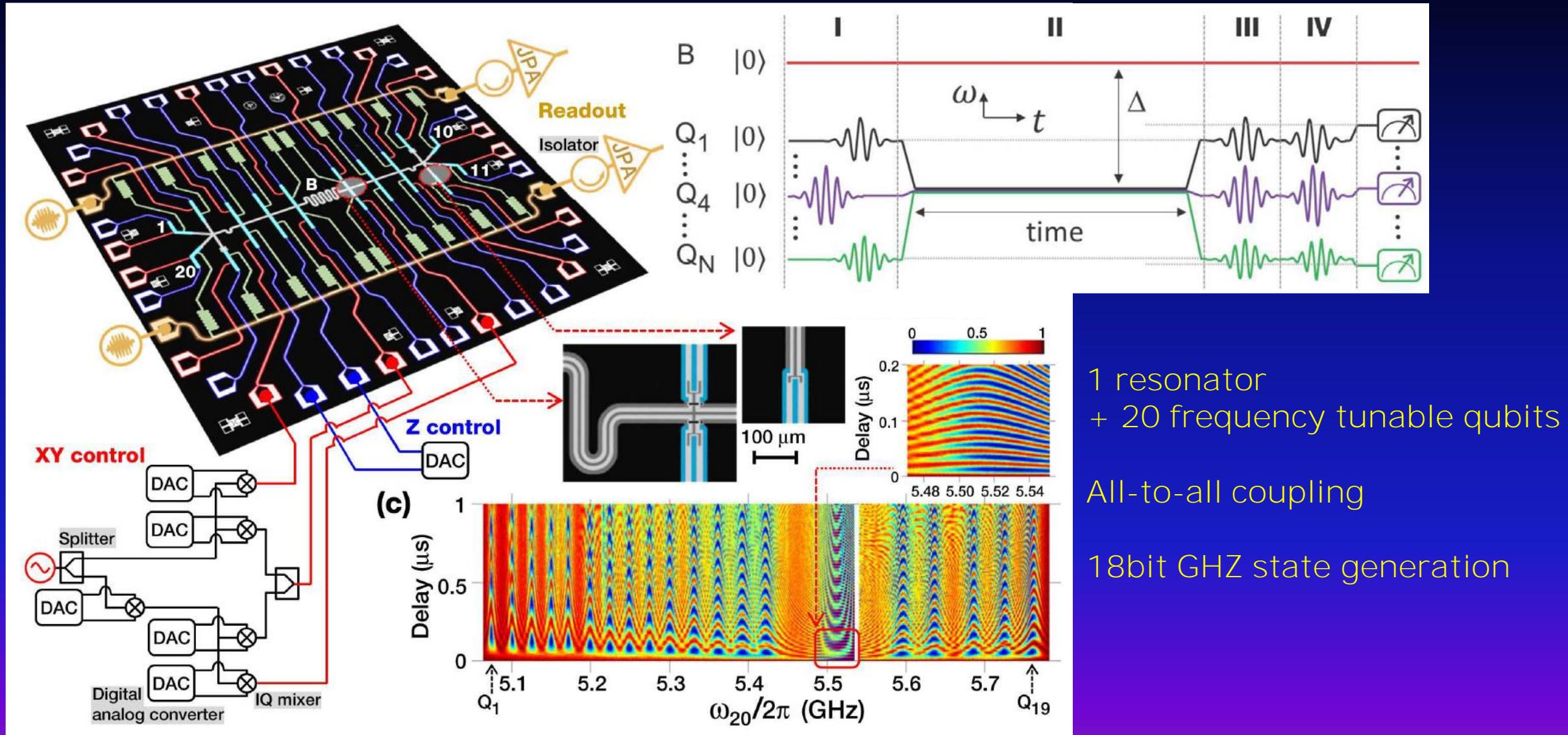
# Resonator mediated coupling



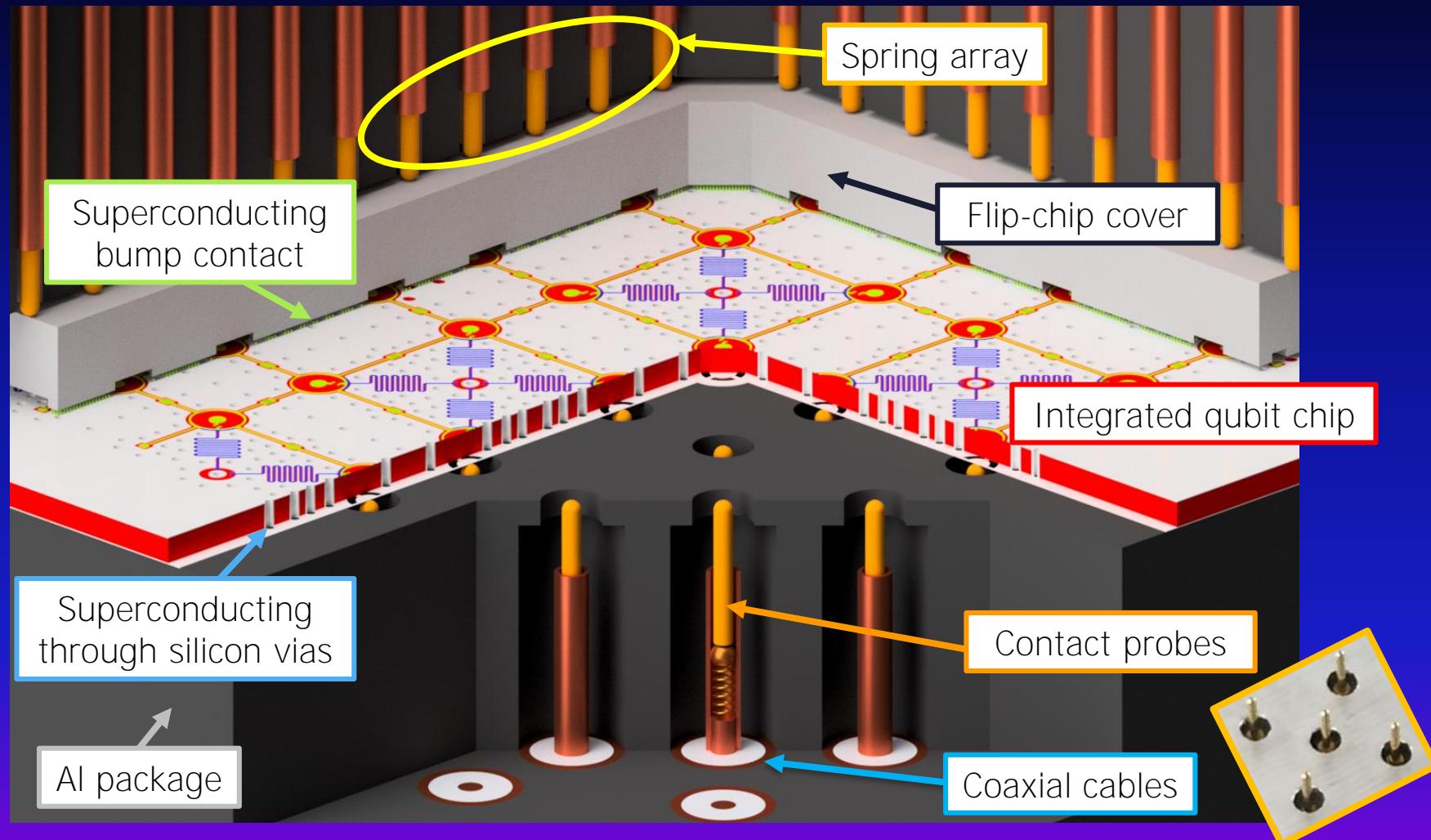
- Tunable qubits
- 49-bit chip under development

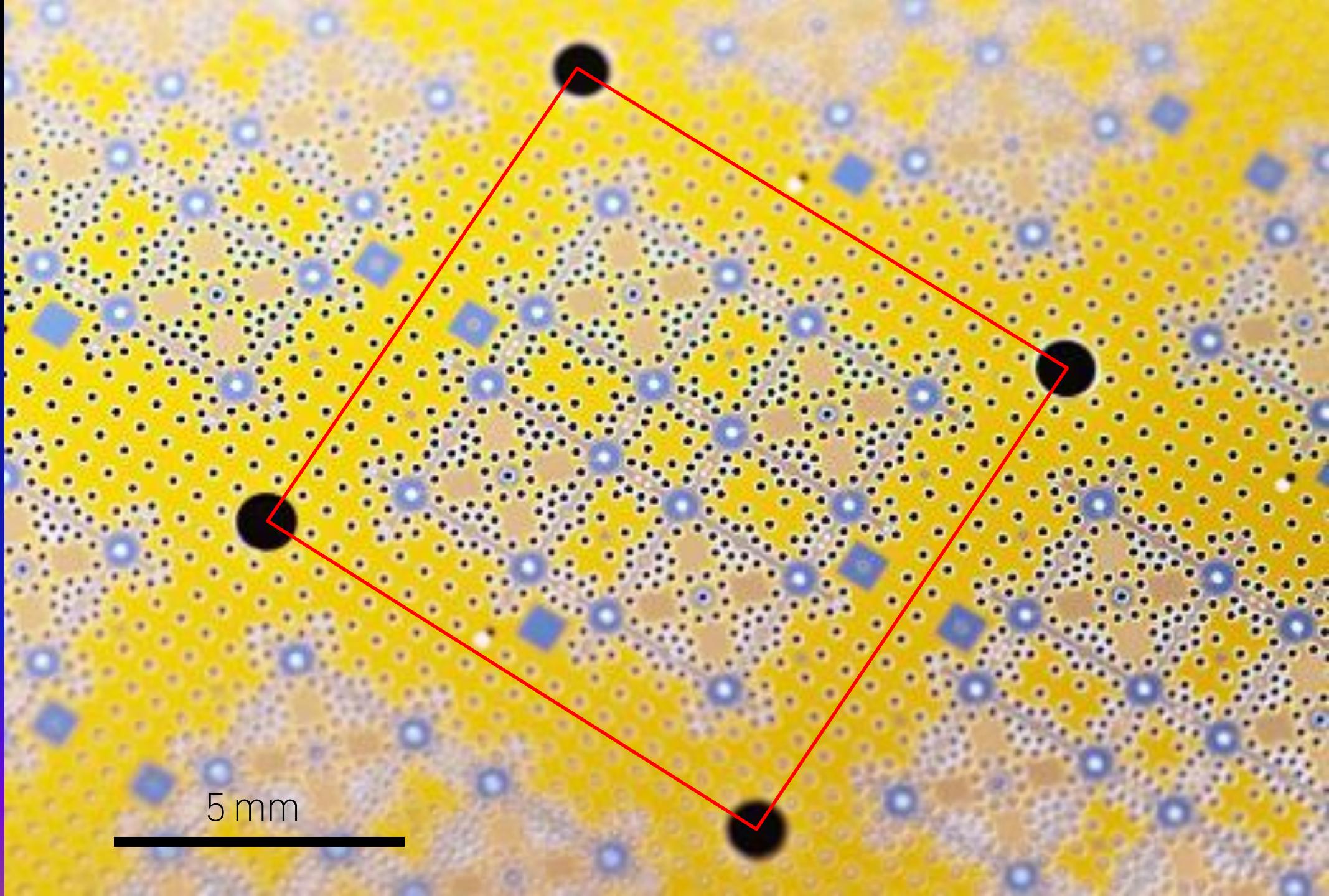


# Quantum bus coupling



# 2D integration with vertical access





# **"Smart"** brute force

Improved coherence  
materials  
fabrication

Fast gate/readout  
design  
architecture

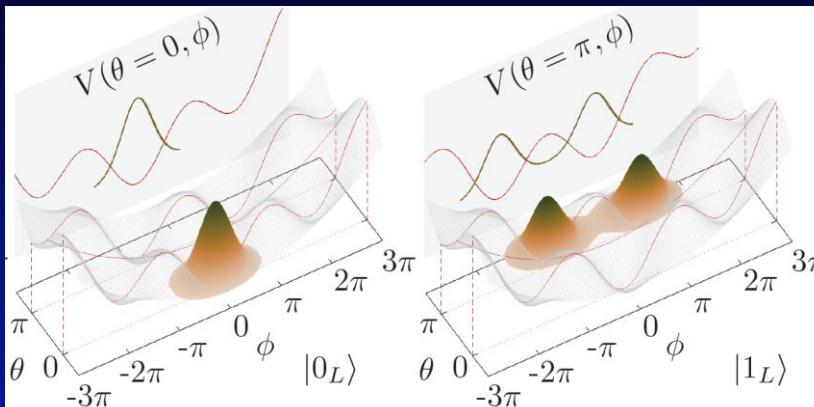
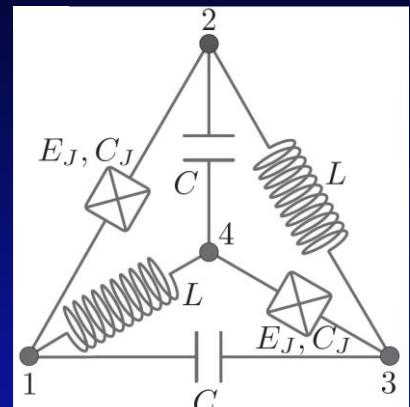
Better packaging/wiring  
filtering, shielding, thermalization  
high-density, miniaturized components

Peripheral technologies  
control electronics  
SFO/CryoCMOS  
cryogenics

low-latency, all-digital?

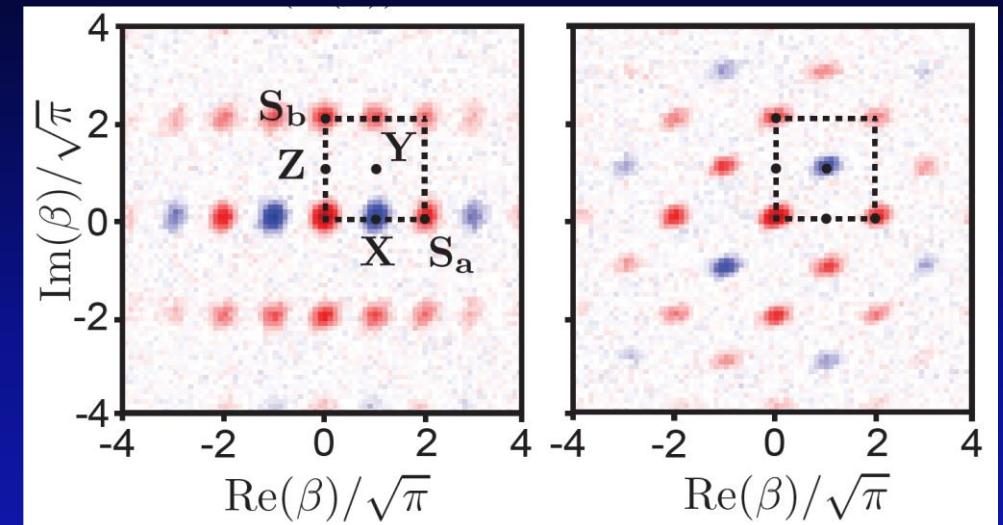
# New directions beyond brute force

Robust qubits (0-■ qubit, etc.)



arXiv:1910.07542 (Princeton)

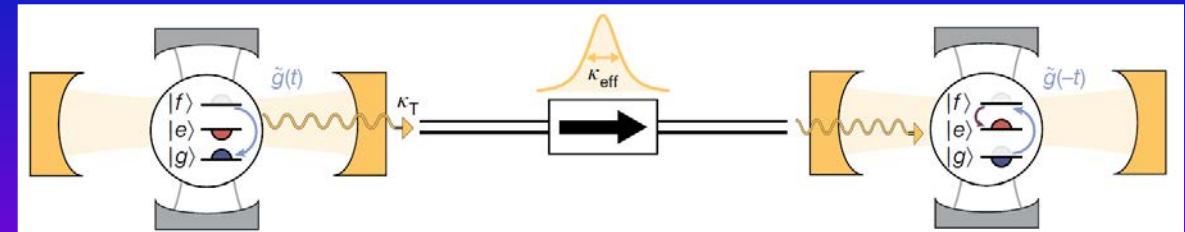
Bosonic codes (cat, binomial, GKP, etc.)



arXiv:1907.12487 (Yale)

Temporal-mode entanglement  
+ Measurement-based  
quantum computing

Distributed quantum computing



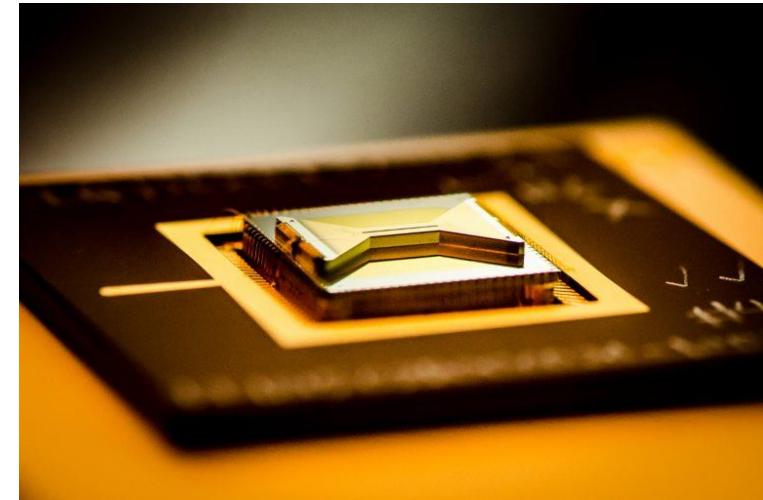
Nature 558, 264 (2018) ETH Zurich

# Current state-of-the-art of ion trap quantum computing

High-fidelity operations achieved in few-qubit systems

- Single-qubit gate: 99.9999 %
- Two-qubit gate: 99.9 %
- SPAM: >99.9%

PRL 113 220501(2014); PRL 117 060504(2016) Oxford



Advantages of using trapped ions:

- Long coherence times (~ 1 min)
- Identical qubits (cf. atomic clocks)
- Room temperature operation
- All-to-all qubit connection

Disadvantages:

- Slow gate speed
- Difficult to scale beyond >50 qubits

**IonQ quantum computer:**

- Fully connected 11 ions
- >60 2-qubit gates w/ error < 1.0%
- Partnered w/ Microsoft Quantum Azure

**Other leading institutions:**

- |                |                               |
|----------------|-------------------------------|
| • NIST         | • U. Maryland                 |
| • U. Innsbruck | • ETH Zurich                  |
| • PTB          | • Honeywell                   |
| • U. Oxford    | • Alpine Quantum Technologies |

# Prospects for ion trap quantum computing

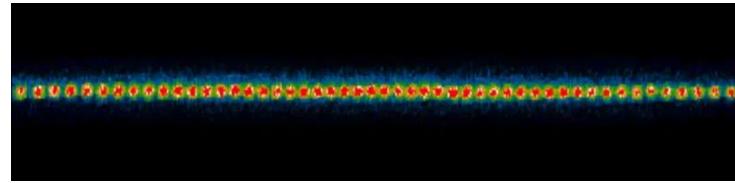


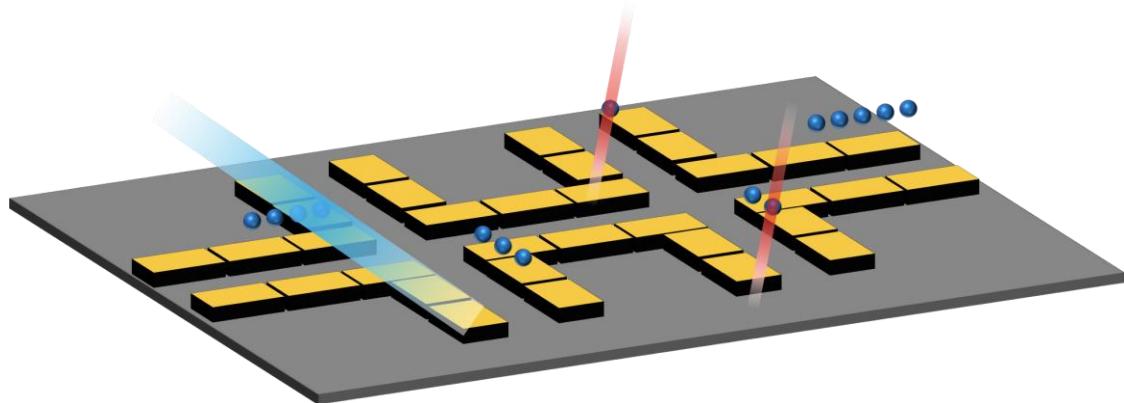
Photo by TIQI group, University of Maryland

Increasing numbers of ions in a 1D chain increases difficulties of quantum operations due to motional mode crowding.



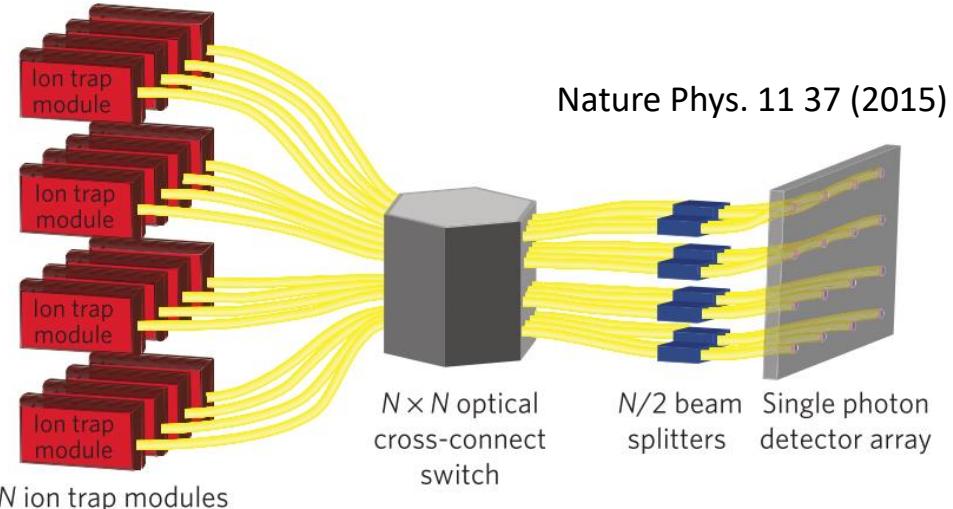
**Multi-trap architecture needed for >100 qubits.**

- **Quantum CCD**



- Separate trapping and interaction zones
- Ion shuttling on 2D electrode structure
- Microwave gate integration
- NIST, PTB, U.Sussex

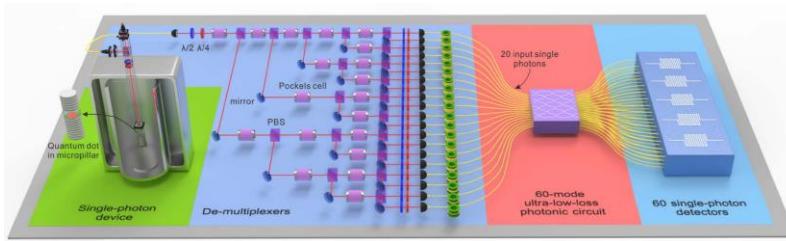
- **Optical interconnect**



- Trap modules connected via photons
- High modularity
- U.Maryland, U.Oxford, U.Osaka

# Recent trends in photonic quantum computing

## Boson sampling

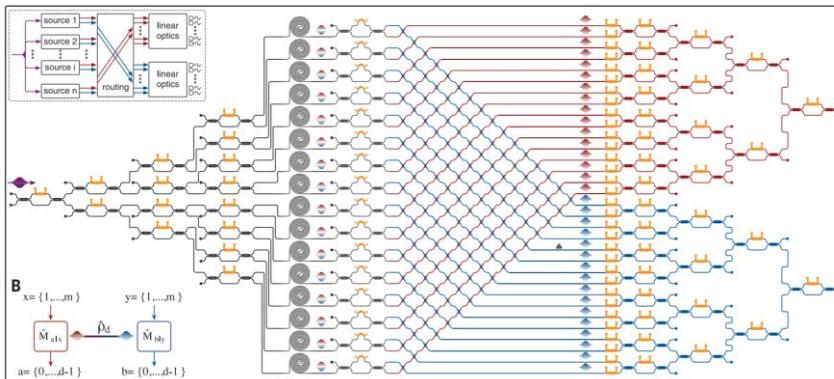


20 photons in 60 modes

arXiv:1910.09930

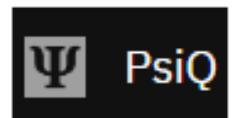
(Univ. of Science and Technology of China)

## Integrated photonic chips



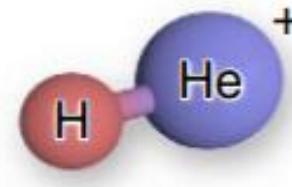
>550 components on a single chip

Science **360**, 285 (2018) (Univ. of Bristol)

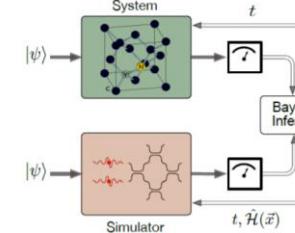


## NISQ algorithms

Variational quantum eigensolver      Quantum Hamiltonian learning



Nat. Commun. **5**, 4213 (2014) Nat. Phys. **13**, 551 (2017)  
(Univ. of Bristol) (Univ. of Bristol)



## Large-scale continuous-variable cluster states



5x5000  
2D cluster states

Science **366**, 373 (2019) (Univ. of Tokyo)

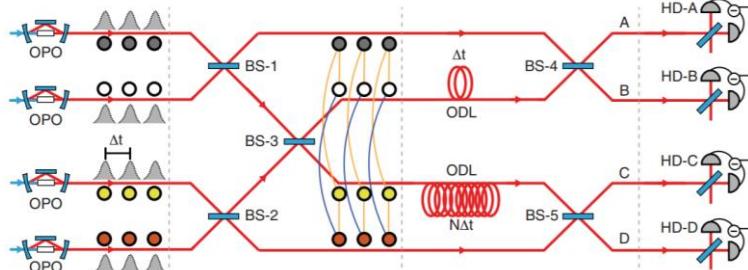
Science **366**, 369 (2019) (Technical Univ. of Denmark)



# Future prospects for large-scale fault-tolerant photonic QC

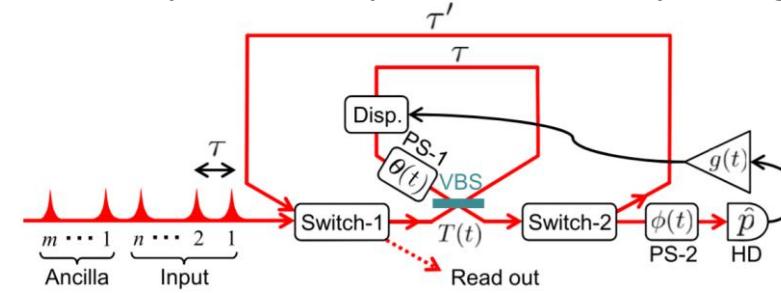
## Architecture for scalable & universal quantum computing

### Time-domain multiplexed one-way quantum computation



Science 366, 373 (2019) (Univ. of Tokyo)

### Loop-based quantum computing



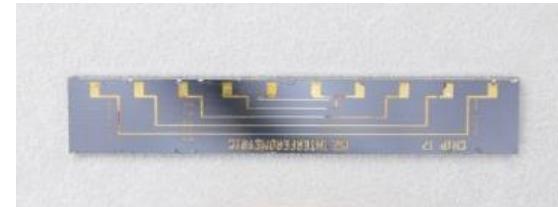
Phys. Rev. Lett. 119, 120504 (2017) (Univ. of Tokyo)

## High quality photonic circuits based on photonic chips & fibers

### Free-space optical circuits



### Photonic chip



Nat. Photon. 9, 316 (2015) (Univ. of Tokyo)

### Fiber delay line



## Bosonic codes for fault-tolerant quantum computation

One optical pulse represents one logical qubit (cat/binomial/GKP code) => efficient!

Example: GKP code + continuous-variable quantum gates

⇒ Realistic threshold for fault-tolerant quantum computation ( $\sim 10$ dB of squeezing)

Phys. Rev. X 8, 021054 (2018) (Hokkaido Univ.)

# of qubit

## Fault-tolerant universal quantum computer

**NISQ** (noisy intermediate scale quantum technology)

9Q@google

22Q@google

19Q@Rigetti

72Q@google

5Q@IBM

20Q@IBM

79Q@IonQ

53Q@IBM

53Q@google

11Q@Alibaba

11Q@IonQ

2014

2015

2016

2017

2018

2019

2020

2021

2022

2047

2048

2049

2050

Quantum computing research will inevitably  
(and hopefully) be as

- disruptive
- controversial
- provocative
- philosophical



as we have seen in the history of quantum mechanics