

#### Moonshot Goal 6: International Symposium 2023 2023/07/18

Project progress report **"Research and Development of Theory and Software for Fault-tolerant Quantum Computers"** 

> Masato Koashi (Project Manager) The University of Tokyo

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- Overview of the Theory/Software Project
- Progress report
  - A prototype of cross-layer codesign model and related results
  - Quantum error correcting codes and fault tolerance
  - Hardware-specific results
  - Toward distributed architectures
  - NISQ and FTQC
- Summary and Outlook

#### Projects in Moonshot Goal 6



Integration Technologies for Superconducting Quantum Circuits

Large-scale Silicon Quantum Computer

Scalable Silicon quantum computer technology

Quantum Computing with Photonically Interconnected Ion Traps

Large-scale quantum hardware based on nanofiber cavity QED

Large-scale and high-coherence FTQC with dynamical atom arrays

Large-scale Fault-tolerant Universal Optical Quantum Computers

Quantum Interfaces for Building Quantum Computer Networks

Quantum Cyberspace with Networked Quantum Computer

Scalable and Robust Integrated Quantum Communication System

Scalable, Highly Integrated Quantum Error Correction System

Theory and Software

# Mission for Theory/Software PJ



Moonshot Goal 6:

Realization of a fault-tolerant universal quantum computer that will revolutionize economy, industry, and security by 2050.



Substantially reducing the device performances required for large-scale faulttolerant quantum computer.

#### Project Pls



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MOONSHOT RESEARCH & DEVELOPMENT PROGRAM

# Mission for Theory/Software PJ



Moonshot Goal 6: Realization of a fault-tolerant universal quantum computer that will revolutionize economy, industry, and security by 2050.



Substantially reducing the device performances required for large-scale faulttolerant quantum computer.



• A fault-tolerant universal quantum computer (FTQC) is a complicated system with many layers, such as designing qubits, controlling qubits, error correction, logical operations, compiling, and applications.

> • A cross-layer codesign model is a set of connected software stacks that encompasses metrics and trade-off relations in these layers, and through optimizations and simulations, predicts the performance of the FTQC.

Performance of FTQC



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• In the development stage of the cross-layer codesign model, it works as a hub to promote research in the Theory/Software Project.

Encouraging interactions among them

Letting them communicate in the same language

Helping them see the big picture toward the Goal of the Moonshot program.



RESEARCH & DEVELOPMENT PROGRAM



• Once the prototype of the model is constructed, it will work as a pilot to guide the researchers (theoretical/experimental) in the Moonshot Program toward its Goal.

> Trade-offs in various levels: Choice of parameters in hardware design Allocation of time and money

Hard to know what is the best within a single layer.

Performance of FTQC



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# Mission for Theory/Software PJ



Moonshot Goal 6: Realization of a fault-tolerant universal guantum computer that will Steady research and revolutionize economy, industry, and security by 2050. breakthrough ideas Performance from participants of devices Accelerating trends "Required" performance Moonshol Cross-Layer codesign models Program "Feasible" performance 2021 2030 2040 2050 Mission for Theory/Software PJ

Substantially reducing the device performances required for large-scale faulttolerant quantum computer.

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### Cross-layer co-design (CLCD) model



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- CLCD models can be used for  $\cdots$ 
  - Revealing the trade-off relations across multiple layers
  - Evaluating of the true impact of a technological improvement
  - Demonstrating current state-of-the-art design by unifying proposed technologies
- CLCD models should be equipped with functions such as  $\cdots$ 
  - Evaluating the performance under a provided FTQC configuration
  - Optimizing parameters of FTQC to maximize the performance
  - Substituting mock-up modules depending on available computational resources





### Managing inhomogeneous qubit qualities

#### Background

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# Low-quality qubits are unavoidable in large-scale integration



Unfavorable bad-qubit allocation results in bad logical qubit.



Low-quality physical qubit

### Managing inhomogeneous qubit qualities

#### **Our contribution**

We developed fast error-estimation circuits on FPGAs considering error-rate variations.

#### Performance evaluation



Liao, Suzuki, Tanimoto, Ueno, Tokunaga, the 28th Asia and South Pacific Design Automation Conference (ASP-DAC), Online (2023).

High-logical-error logical qubits appear with the existing error-estimation methods. Our proposal successfully reduce them.

**Key technology** For fast error-estimation, we partially utilize pre-calculated look-up tables in a scalable way.





(NTT, U of Tokyo)

### Burst-error tolerant FTQC

#### Background



Typical QEC schemes assume uniform error rates in error estimation Current QEC scheme assumes error properties are static. However, they can vary during long-term computation.



#### Phenomena resulting in time-varying error properties

Cosmic-ray bust errors



Property of cosmic-ray strike

Duration: 26ms Area: 4x4 qubit area Error rate: x30 Frequency: 0.1Hz in 26-qubit chip area

M.McEwen et al., NatPhys 18, 107-111 (2022)

FTQC is expected to run for several hours to show asymptotic speed-up. Thus, time-varying properties can spoil the scalability of FTQC architecture.

### Burst-error tolerant FTQC



#### Key idea: Quickly detect time-varying errors and mitigate them with two strategies

Detect time variation with anomaly detection

Expand code distances of affected regions

Re-estimate errors with considering detection







#### We designed FTQC control units and show its feasibility with FPGAs



Resource usage

Configuration	FF (%)	LUT (%)	throughput
40 – <b>BASE</b>	8,991 (4)	14,679 ( 6)	4.66
40 – Q3DE	13,855 ( 6)	20,279 (9)	4.25
80 - BASE	13,211 ( 6)	36,668 (16)	1.81
80 – Q3DE	22,751 (10)	54,638 (24)	1.79

Memory and latency overhead.

Unit	Order	Size
syndrome queue active node counter	$\frac{2d^2(c_{\rm win} + \sqrt{2c_{\rm win}})}{2d^2\log_2 c_{\rm win}}$	623 kbit 16 kbit
matching queue	$2d^2\sqrt{c_{\rm win}/2}$	24 kbit
inst. hist. buffer	negligible	-
expansion queue	negligible	-

### Burst-error tolerant FTQC

(NTT, U of Tokyo)

#### Baseline evaluation and our contribution

Hardware requirement to achieve  $10^{-10}$  logical error rates under cosmic-ray burst errors. (Achievable hardware configurations are shaded)



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#### Improving connectivity of surface code via dynamic code concatenation



(Osaka U)



- Can be implemented with qubits aligned on a twodimensional plane or square lattice.

- Error tolerance is high, up to about 1%.



Google team, Nature 574 510 (2019)

- Coupling with adjacent 4 qubits makes frequency collision issue and causes error via crosstalk.

-IBM proposed a method using a graph of order 3 (performance is not so good because it uses subsystem code)



C. Chamberland et al, Phys. Rev. X 10, 011022 (2020)

Improving connectivity of surface code via dynamic code concatenation

- The proposed method is based on dynamic concatenation of small distance codes and surface codes.
- Surface codes can be constructed on a graph of **degree 3**.
- The small distance code can **detect errors and raise a flag**.
- Constructed an accurate decoder using the flag information.





(Osaka U)

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#### Improving connectivity of surface code via dynamic code concatenation



#### (Osaka U)





- The threshold values are almost the same as that for the conventional method with degree 4 graph.
- Higher threshold and lower logical error rate compared to previous studies with other codes.

### Decoding color code by simulated annealing



- Transversal gates are supported for all Clifford gates.
- The threshold is one order of magnitude smaller when circuit-level noise is considered.

Decoding is difficult, and even performance evaluation is not so easy.

**Proposed method:** Assign a classical spin variables to each stabilizer, and find the error location with the lowest weight by minimizing cost functions.

#### Error decomposition : E = T(S)GL

T(S): A fixed error (pure error) defined by *S*.  $G = \prod_{i} G_{i}^{g_{i}}$ : Stabilizer operator  $G_{i}$  and corresponding binary valuable  $g_{i}$ .

 $L = L_X^{l_X} L_Z^{l_Z}$ : Logical operator  $L_X$ ,  $L_Z$  and corresponding binary valuables.

 $\rightarrow$  Find  $\{g_i\}, l_x, l_z$  that minimize the weight wt(E) by higher order lsing optimization problem.



(Osaka U)

Y. Takada et al., arXiv:2303.01348.

# Decoding color code by simulated annealing

Comparison of CPLEX and SA for exact optimal solution



#### →Comparable to exact solver, CPLEX. SA $0.027 \sec 0.027 \sec 0.065 \sec$ CPLEX $0.059 \sec 0.289 \sec 0.453 \sec$

#### Thresholds by existing decoders

Renormalization group decoder [3]	8.7%
Union find decoder [4]	9.8%
Restriction decoder [4]	10.2%
Least-weight correction [5]	10.6%

#### Future direction:

- Extend the system to handle more realistic noise.
- Investigate a method to measure the syndrome of color codes without degrading the threshold.

(Osaka U)

### Local probabilistic decoding





T. R. Scruby and K. Nemoto, arXiv:2212.06985.



• Commonly assumed in the literature:

A useful quantum decoder should be able to correct all "constant weight" errors

The simple <u>"flip" decoder wouldn't work</u> Very fast. The error pattern search is locally done by flipping only one bit at a time.

• But.. that's not the case!!

A novel decoder for the 3D Toric code:

A novel decoder for the 3D Toric code:

	Runtime	Threshold
Proposed	0(n)	5.5%
Best Known*	$O(n^{3})$	7.1%

(\*) Higgott and Breuckmann, arXiv.2206.03122.

### Efficiency of FTQC in the large-scale limit



(U of Tokyo)

 Overheads for fault-tolerant quantum computing The actual FTQC circuit *M*: size of the problem The circuit to solve the problem Compiling # of logical qubits # of physical qubits W(M) = O(poly(M)) $W(M) \times ???$ D(M) = O(poly(M))Space overhead  $D(M) \times ???$ Time overhead # of logical steps # of actual steps

#### Concatenated codes

Nesting the same small code repeatedly.



Quantum LDPC codes (with a nonzero rate) A bigger code

with a larger distance and with more logical qubits



Needs concatenated codes to assist a gate operation. Gates cannot be operated cannot be in parallel if one wants to save the space overhead. Time overhead

poly(M)

### Efficiency of FTQC in the large-scale limit

• A novel architecture of concatenated codes:

Space overhead O(1)

7-qubit code

Time overhead  $\exp(O(\operatorname{polylog}(\log M))) \ll \exp(O(\log M)) = \operatorname{poly}(M)$ 

Instead of repeating the same code in the conventional
 Decoding is done in each level and is very simple.
 (The threshold and the overhead scaling were

we use a series of gradually growing sizes of codes (Quantum Hamming codes)

The code distance is always 3

The number of logical qubits increases and the rateapproaches unity.15-qubit code

Concatenation of these codes forms a big code with a non-zero rate.

(The threshold and the overhead scaling were analytically proved even with the runtime of classical computation is taken into account.)

#### Fault-tolerant gadgets for multiple logical qubits (registers)





(U of Tokyo) Yamasaki and Koashi, arXiv:2207.08826.

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#### Simulation/optimization tools for various physical systems

#### QuTiP (the Quantum Toolbox in Python) (RIKEN)

Open-source numerical package for the general simulation of open quantum systems

More than 1 million downloads

A new package (qutip-qip), which addresses pulse-level simulation of noisy quantum circuits B. Li et al., Quantum 6, 630 (2022).



#### Efficient backcasting search for optical quantum state synthesis

exponential.



(U of Tokyo)

(TMDU) Simulation method for dynamics of superconducting qubits interacting with optical modes with large amplitudes.



(b) Spin chain model

(c) Superconducting qubit model

(d) Optimal control model

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### Super absorption by multiple qubits



(Tsukuba U)

S. Kamimura *et al.*, Phys. Rev. Lett. **128**, 180602 (2022).
 S. Kamimura *et al.*, to appear in Phys. Rev. Lett (2023).

Cooperative effects of N qubits (atoms) under a coherent drive.



The N atoms oscillate in sync with the drive, leading to enhanced effects.

Cooperative effects of N qubits (atoms) without a coherent drive?



#### **Dicke superradiance**





### Super-absorption by multiple qubits



(Tsukuba U)

S. Kamimura *et al.*, Phys. Rev. Lett. **128**, 180602 (2022). S. Kamimura *et al.*, to appear in Phys. Rev. Lett (2023).



### Toward distributed architectures

(NTT、U of Tokyo)

Phys. Rev. A 105, 062432 (2022).

probe  $\alpha/\sqrt{T_B}$ 

aubit B

Displacement

(Keio U)

Bob

Azuma, Koashi, and Imoto,

probe

BS(50:50

Clair

 $\left| \alpha / \sqrt{T_A} \right\rangle$ 

aubit A

Alice



## Optimal entangle distribution via dispersive matter-light interaction

• Entangling remote qubits via light:

Dispersive interaction between a qubit and a "classical" light pulse (coherent state).  $\hat{V}|0\rangle_A|\alpha\rangle_a = |0\rangle_A|\alpha_0\rangle_a$ Lossy optical channel.  $\hat{V}|1\rangle_A|\alpha\rangle_a = |1\rangle_A|\alpha_1\rangle_a$ 

 Found the optimal yield of entanglement (largely independent of applications)

• The optimality is achievable with photon number parity measurements

# Distributed quantum error correction for chip-level catastrophic errors

• Quantum error correction across separate chips in a distributed quantum-computing setup.

• Even if arbitrary data in a chip is destroyed, by connecting data chips and an ancilla chip, the data can be retrieved.

Xu, Seif, Yan, Mannucci, Ousmane Sane, Van Meter, Cleland, Jiang, Phys. Rev. Lett. **129**, 240502 (2023).

Demonstration of SWAP gate between a photon qubit and a superconducting qubit

 Very simple method: Reflecting a microwave photon qubit on a superconducting qubits.

• A fidelity of ~0.8 was demonstrated.

 (TMDU with AIST in Yamamoto superconducting PJ)



Koshino and Inomata, arXiv:2302.04548.



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### Bridging the gap between NISQ and FTQC





Bridging the gap between NISQ and FTQC

Akahoshi, Maruyama, Oshima, Sato, Fujii, arXiv:2303.13181





### Error mitigation meets the error correction

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Probabilistic error cancellation (PEC) Temme, Bravyi, Gambetta, Phys. Rev. Lett. 119, 180509 (2017). (NTT, Osaka U)
PEC is a method of quantum error mitigation to reduce the effect of noise in computation
Without probabilistic error cancellation
With probabilistic error cancellation



$$\begin{split} \rho & - \mathcal{U} \qquad \mathcal{N}^{-1} - \mathcal{I} \\ \underset{\text{preparation}}{\overset{\text{State}}{\text{Gate}}} & \underset{\text{Noise}}{\overset{\text{Recovery}}{\text{Recovery}}} \underset{\text{Measurement}}{\overset{\text{Measurement}}{\text{Measurement}}} \\ \langle \Pi \rangle_{\text{qem}} &= \langle \langle \Pi | \mathcal{N}^{-1} \mathcal{N} \mathcal{U} | \rho_0 \rangle \rangle = \langle \langle \Pi | \mathcal{U} | \rho_0 \rangle \rangle \\ \underset{\mathcal{N}^{-1} \text{ is not physical, but a linear combination of physical processes.}}{\overset{\mathcal{N}^{-1}}{\text{State}}} \\ \mathcal{N}^{-1} &= \sum_i \eta_i \mathcal{B}_i \text{ for } \eta_i \in \mathbb{R} \text{ and } \mathcal{B}_i \text{ is physical} \\ \langle \Pi \rangle_{\text{qem}} &= \sum_i \eta_i \left( \rho - \mathcal{U} - \mathcal{U} - \mathcal{N} - \mathcal{B}_i - \mathcal{I} \right) \\ \underset{\text{Attach coef. in post-processing}} \overset{\text{Noise}}{\text{State}} \\ \end{split}$$

What is actually obtained:

$$\langle \Pi \rangle_{\text{noisy}} = \langle \langle \Pi | \mathcal{N} \mathcal{U} | \rho_0 \rangle \rangle$$

We can trade "bias" and "variance" with PEC. Errors by variance can be reduced with more sampling.



#### Error mitigation meets the error correction



(NTT, Osaka U)

**Our contribution** We unified QEM and QEC on the equal footing.

Suzuki, Endo, Fujii, Tokunaga, PRX Quantum **3**, 010345 (2022).

Reduce required code distances by applying QEM to error-estimation errors in QEC.

- Errors on the logical qubits tend to be approximated by Pauli errors. S. Bravyi, et al., *npj Quant. Inf.* 4.1 (2018).
- Why not making full use of the Pauli frame.

No change in quantum gates. Error cancellation is done by classical processing only.



Reduce # of T-gates by applying QEM to approximation errors in Solovay-Kitaev decompositions.



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### Summary and outlook of the project



#### Summary of the current progress

• We have constructed connected software stacks including all the layers of FTQC from hardware to software, which is a prototype of the cross-layer co-design model.

• The model has already contributed to several research outcomes that are impossible without the model, such as a proposal of a solution across multiple layers.

• The participants of the Project in each layer are also steadily developing various approaches toward reduction in hardware requirements for realizing a fault-tolerant quantum computer.

• They cover many grounds, such as improving the mainstream methods, pioneering new possibilities, and deepening the fundamental understandings.

#### Outlook

• We plan to improve the software stacks such that they enable co-designing and make them a really useful tool. At the same time, we will continue various attempts in each technology layer for reducing hardware requirements.

• Eventually, we expect cycles of harvesting and cultivating of the co-design model, in which researchers exploit the model to accelerate their research and feed back the outcomes to help the evolution of the model itself.

#### Project Pls



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#### Thank you.