

Landscape of materials design for future nano electronics And high-throughput materials exploration

Toyohiro Chikyow
Advanced Electric Materials Center,
National Institute for Materials Science

T.Nagata¹, N.Umezawa¹, M.Yoshitake¹,
National Institute for Materials Science (NIMS)

K.Ohmori,³, T.Yamada³,
Waseda University

H. Watanabe⁴ ,
Osaka University,

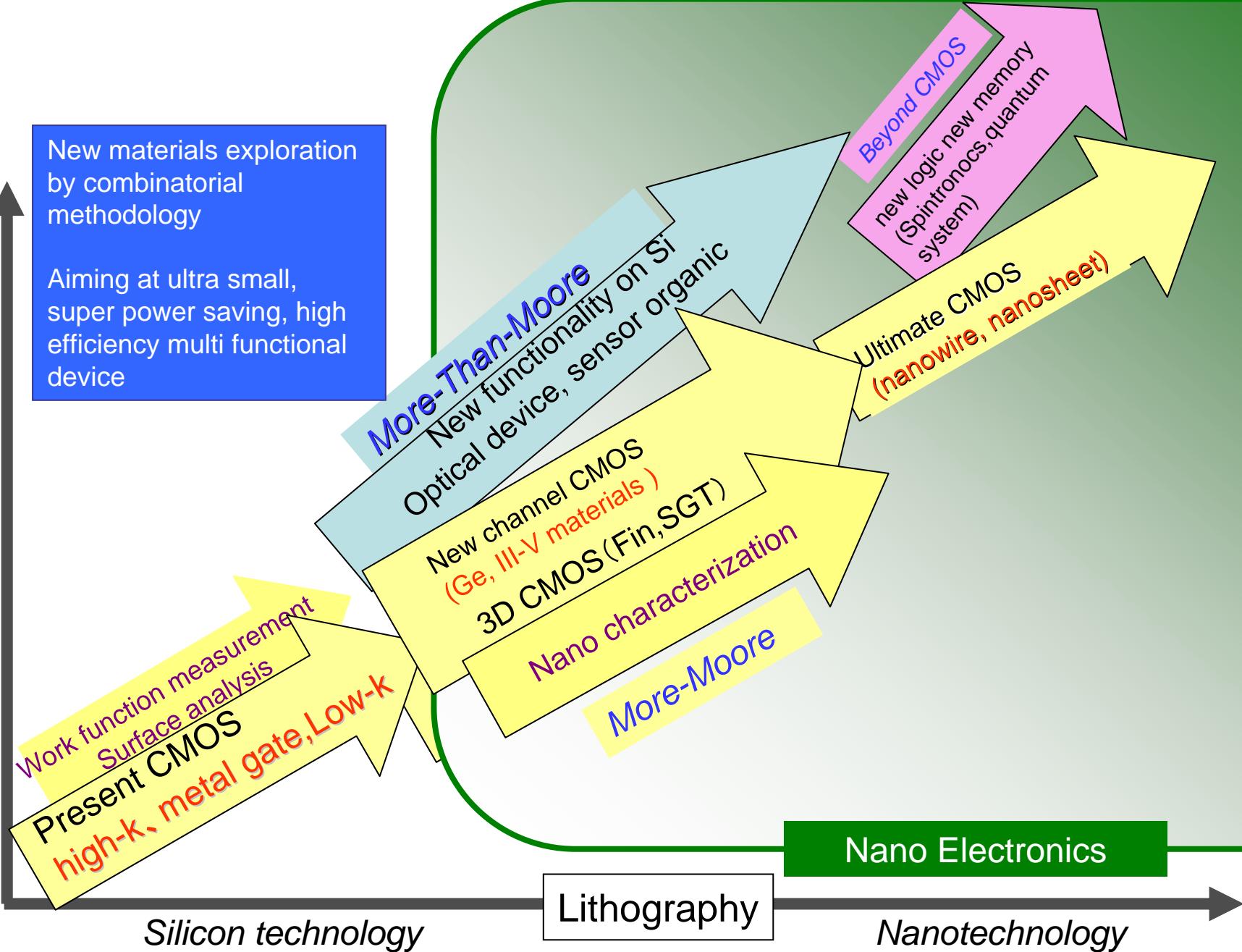
K.Shiraiishi ⁵
⁵*Tsukuba University,*

H. Koinuma^{1, 2}
Japan Science and Technology Agency (JST),

Contents

- 1)New Materials and High Throughput Materials Exploration*
- 2)Example 1: Gate oxide research*
- 3)Example 2 :Metal gate research*
- 4)Materials informatics*
- 5)Summary*

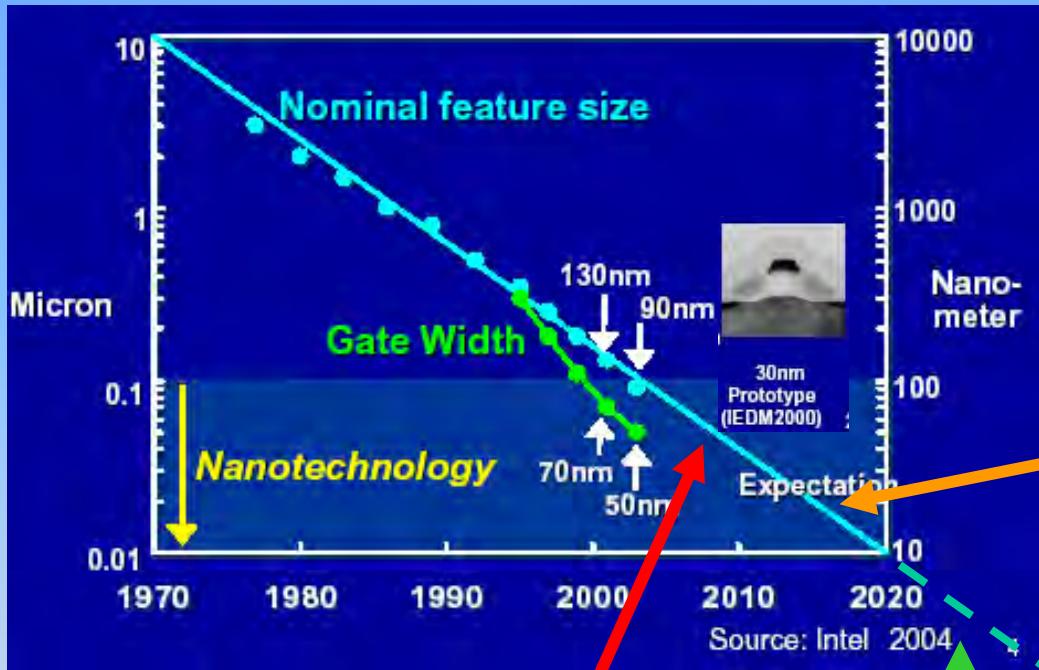
High speed, power saving and functionality



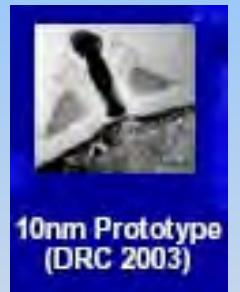
Development of ultra small, super power saving, high efficiency multi function
by integration with Si device technology and nano technology

Research Trend in Si nano device

High speed operation, High density packing, multi –function device



New Materials based Si device (hp32-22 nm node)
Materials
Exploration for Nano device
High-k ,metal gate,
Nano wires (Si CNT or others
interconnection etc)

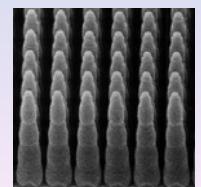


Present Si nano device
(hp65nm node is completed)



Si,Al, SiO₂ have been the major materials

Postscaling generation
• Mixing technology of top down and bottom up
• 3D nano structure
• Variety of nano interface
3D nano structures



New Materials in Future ULSI

National Institute for Material Science



TiN, TaN, NiSi

Ru, W, Mo

TaC

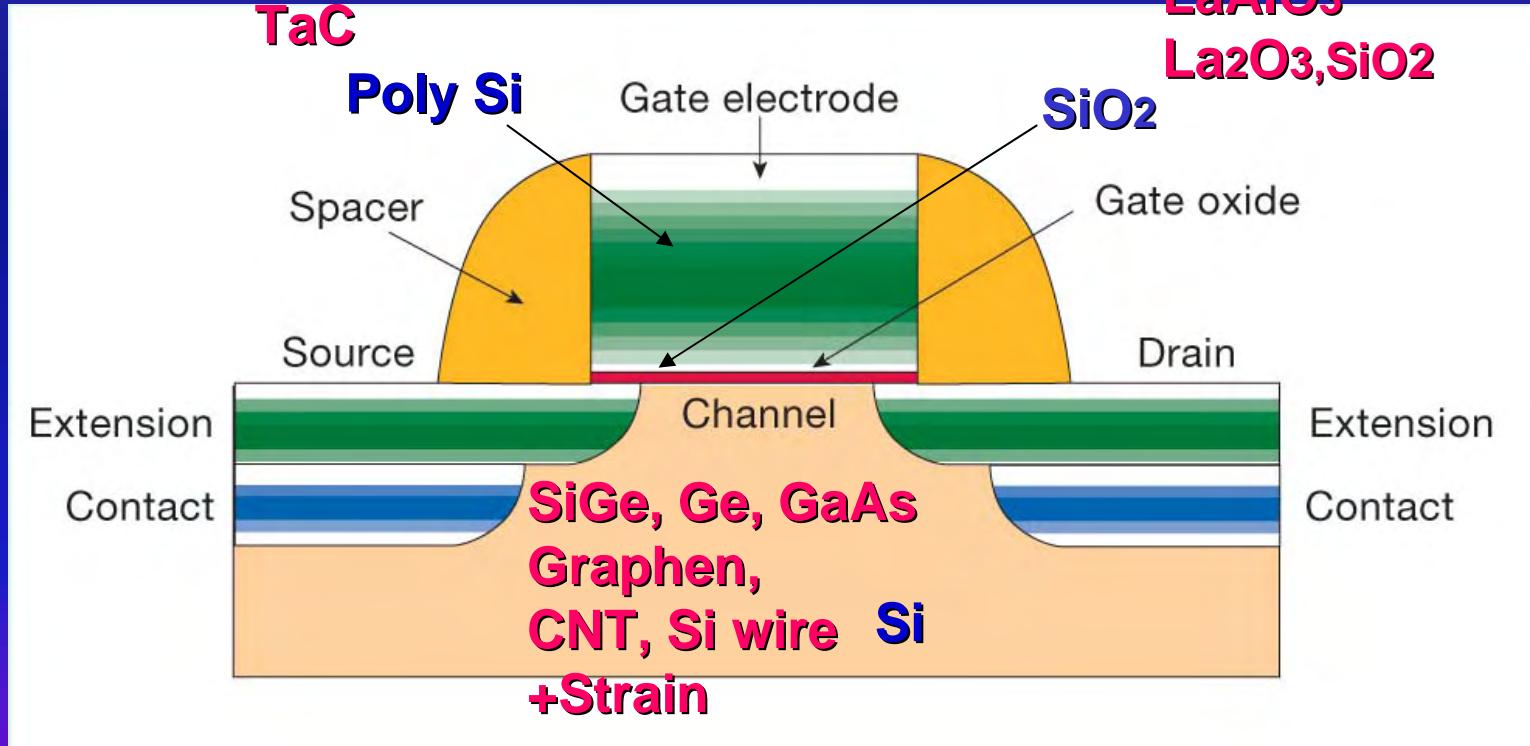
Poly Si

HfSiON

HfLaON

LaAlO₃

La₂O₃, SiO₂

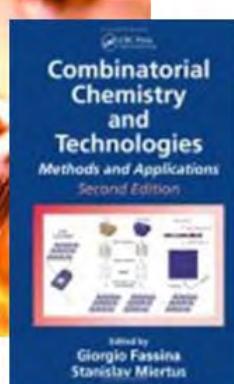


1) Collaboration

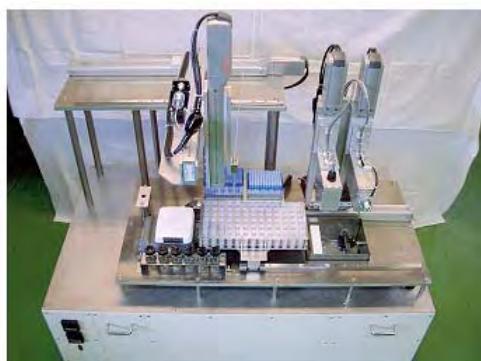
2) HT Experimentation

High-Throughput synthesis : imitation to innovation

Combinatorial Chemistry



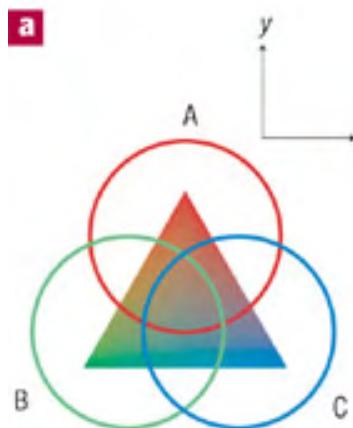
Former combi system for inorganic materials



Innovative thin film technology

"Combinatorial solid-state chemistry of inorganic materials"

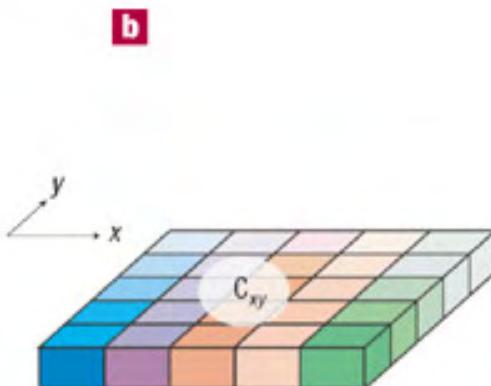
Hideomi Koinuma and Ichiro Takeuchi, *Nature Materials* 3, 429 - 438 (2004)



1st generation

Natural composition spread

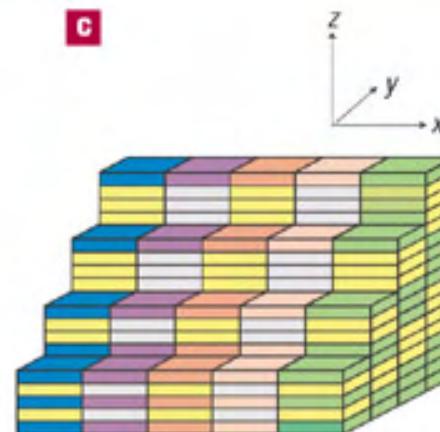
(1965)



2nd generation

Spatially addressable library

(1994)

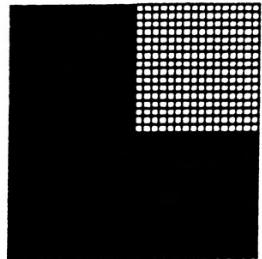


3rd generation

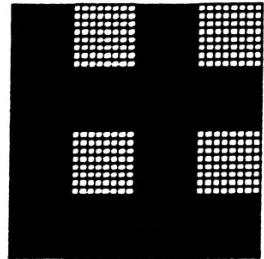
Layer-by-layer controlled array

(1998)

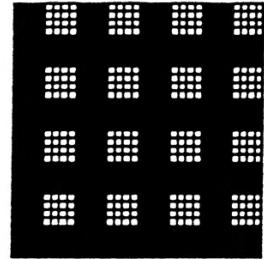
What is combinatorial materials exploration?



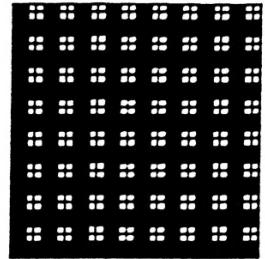
A_i



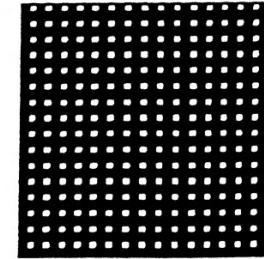
B_i



C_i



D_i



E_i

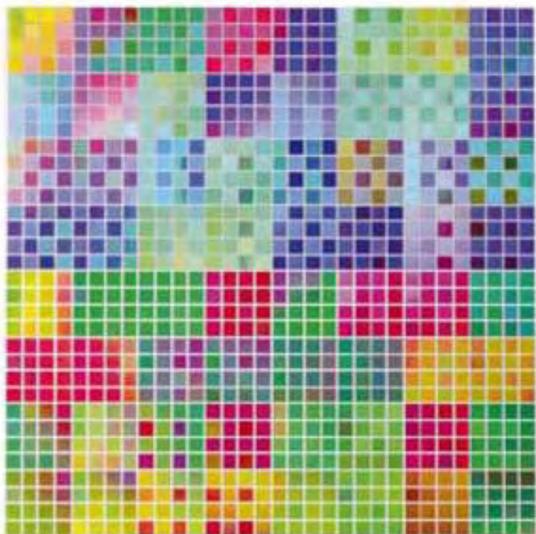
2² (4)

2⁴ (16)

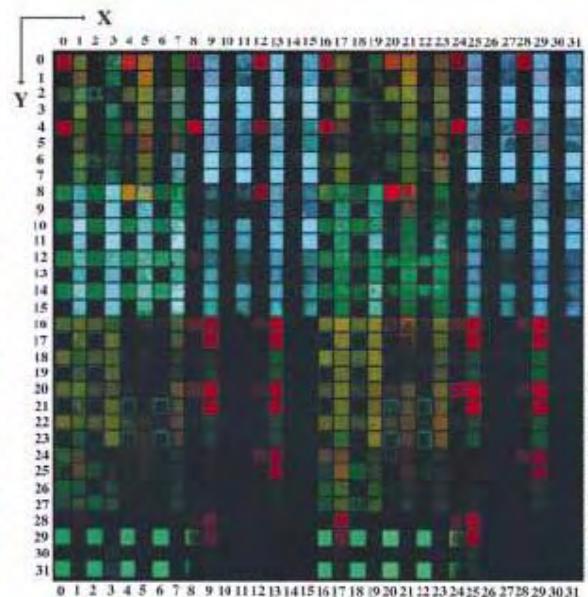
2⁶ (64)

2⁸ (256)

2¹⁰ (1024)



Ambient light



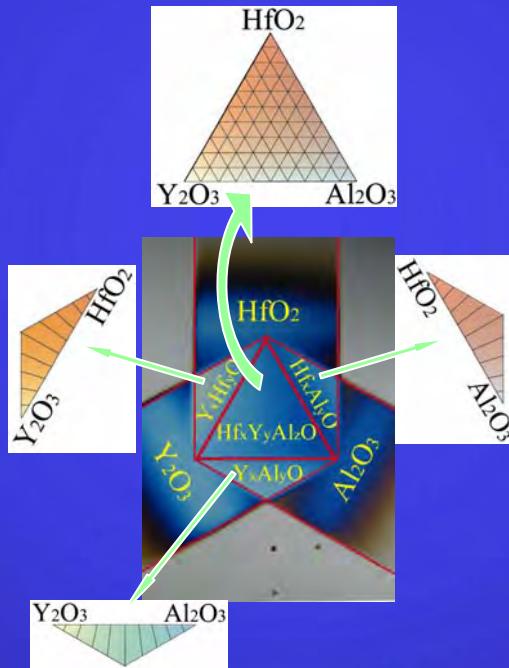
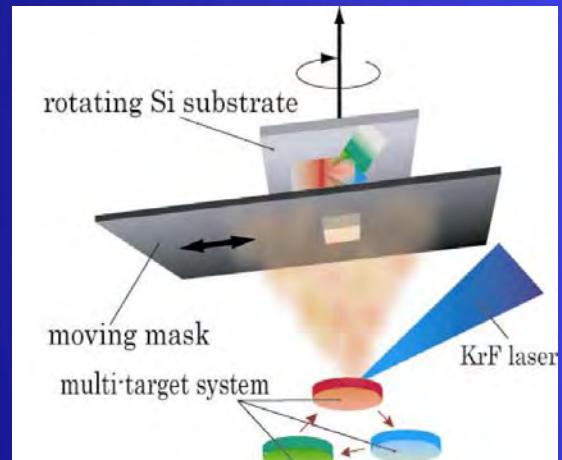
UV irradiation

Discovery of new fluorescent materials

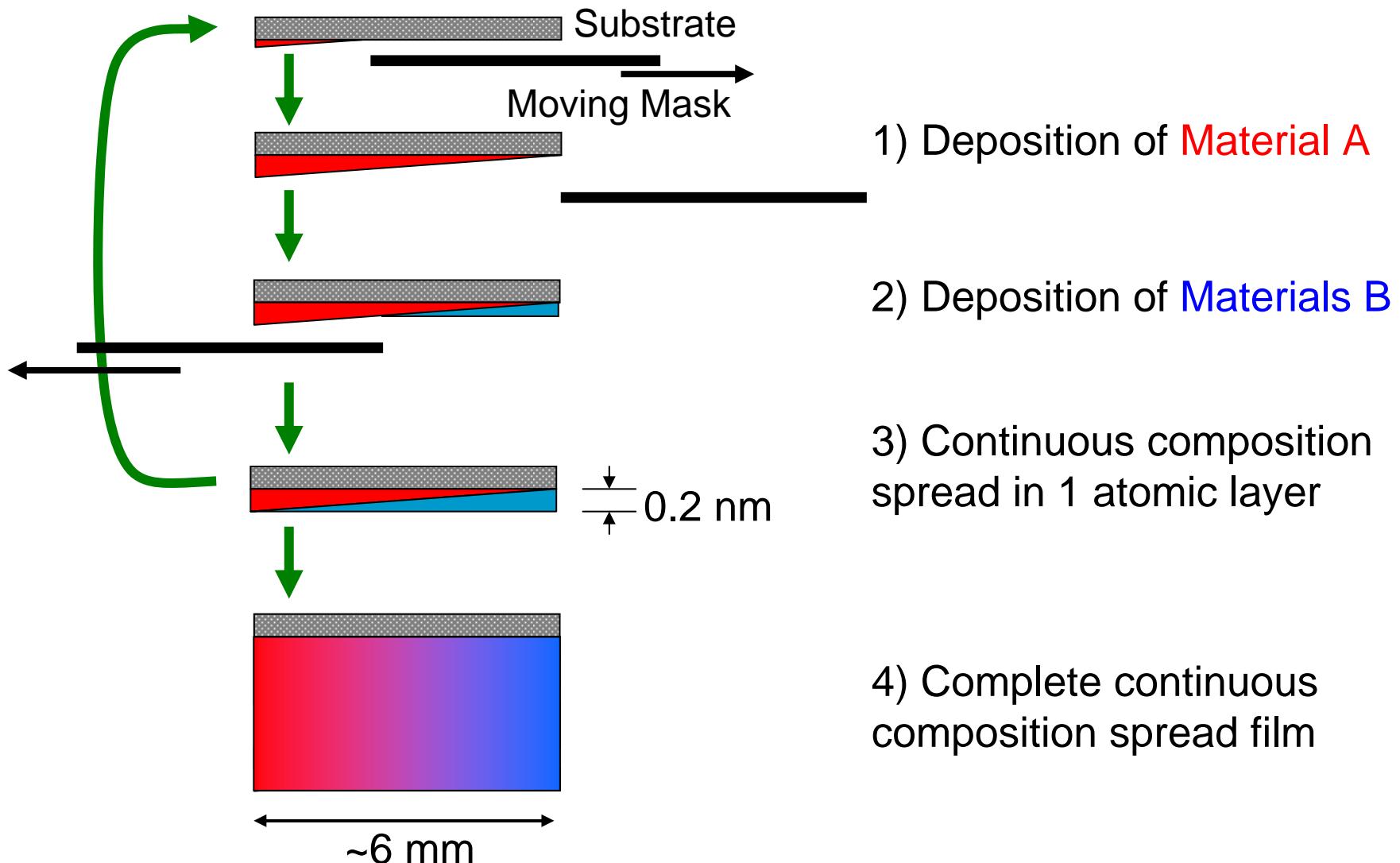
Fig. 2. (A) Photograph of the as-deposited quaternary library under ambient light. The diversity of colors in the different sites stems from variations in film thicknesses and the optical indices of refraction. (B) Luminescent photograph of the processed quaternary library under irradiation from a multiband emission UV lamp at short wavelength (centered around 254 nm).

Jingsong Wang et al *Science* 1998
March 13; 279: 1712-1714

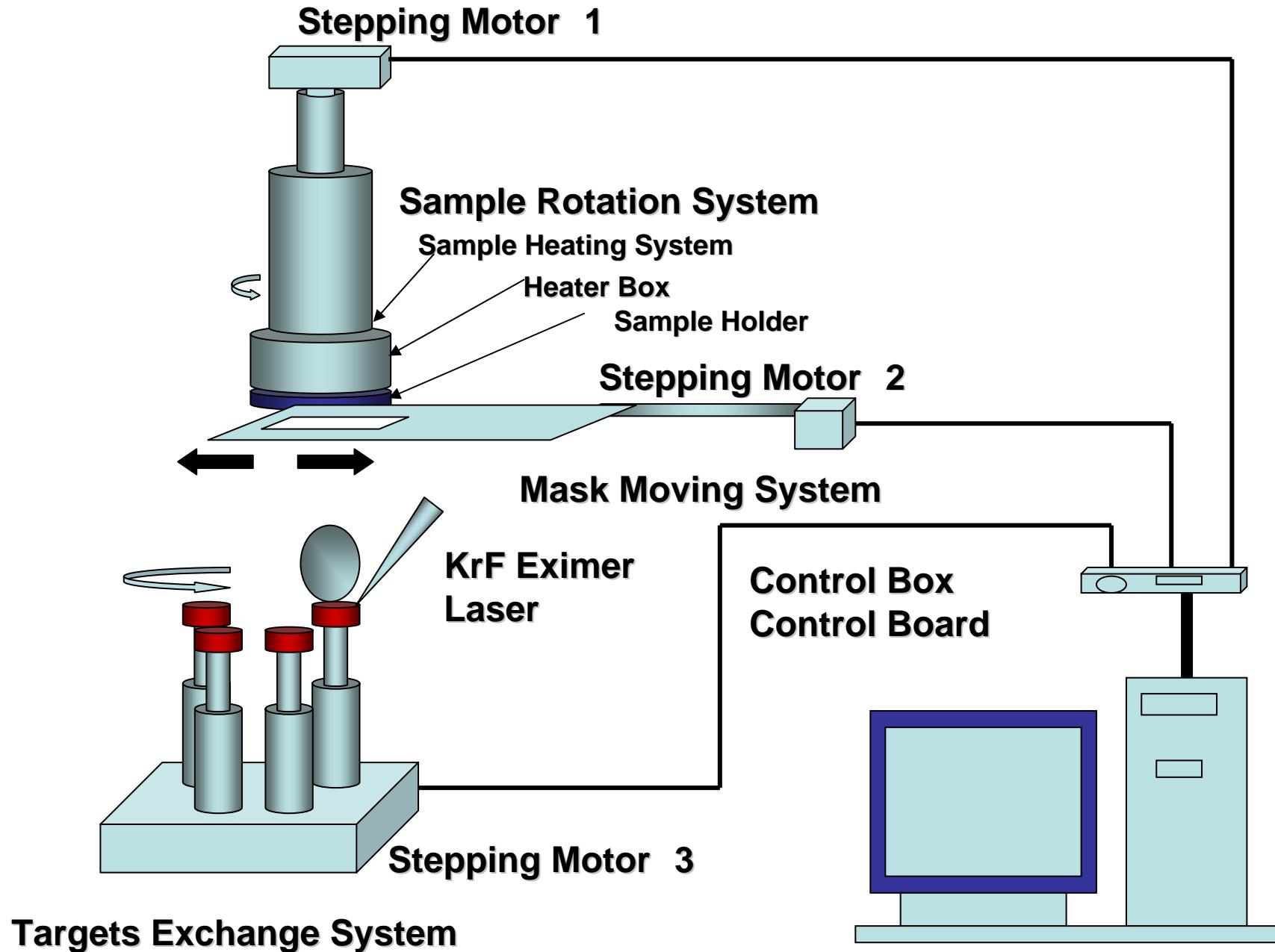
Combinatorial automatic ternary and binary Composition spread synthesis system with Moving Mask System



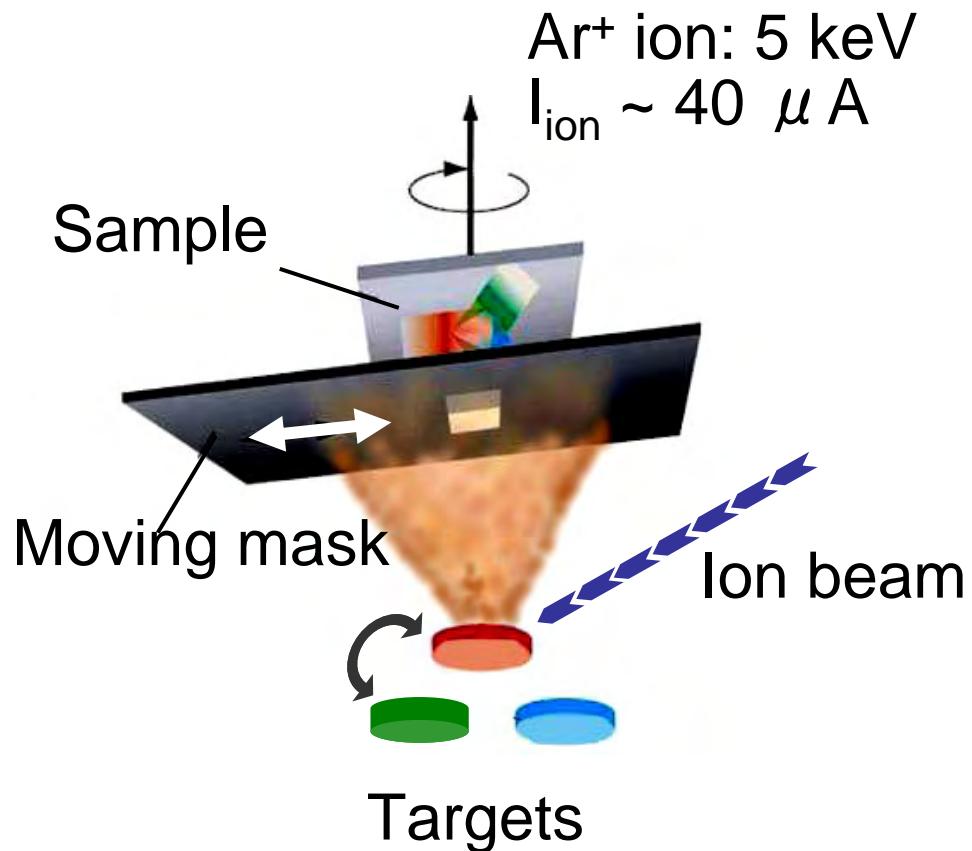
Schematics of binary composition spread film



Concept of ternary materials combinatorial synthesis



Combinatorial deposition systems (@NIMS)



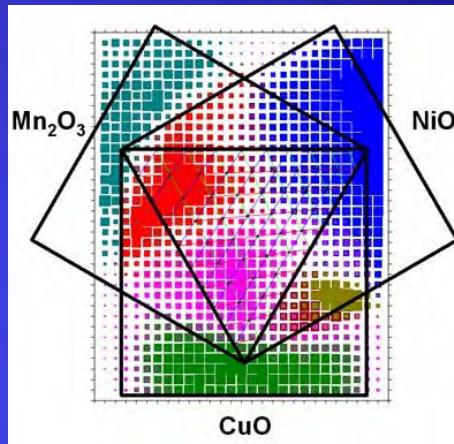
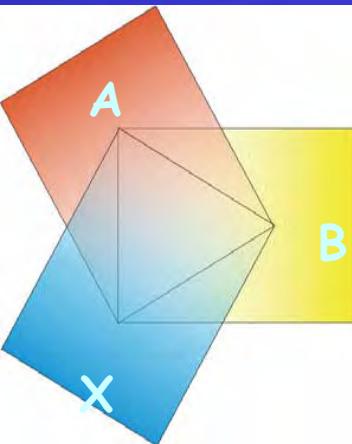
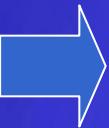
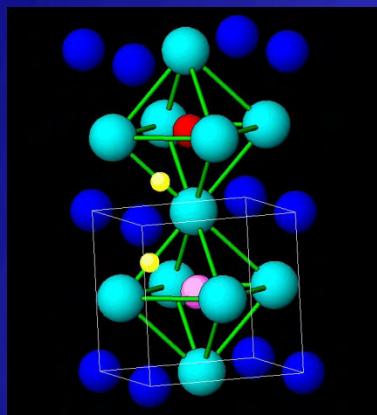
Ion sputtering (metal gate materials)



Pulsed laser deposition (oxides, high-k films)



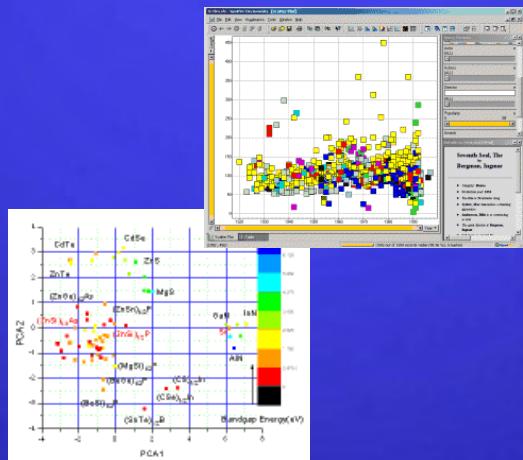
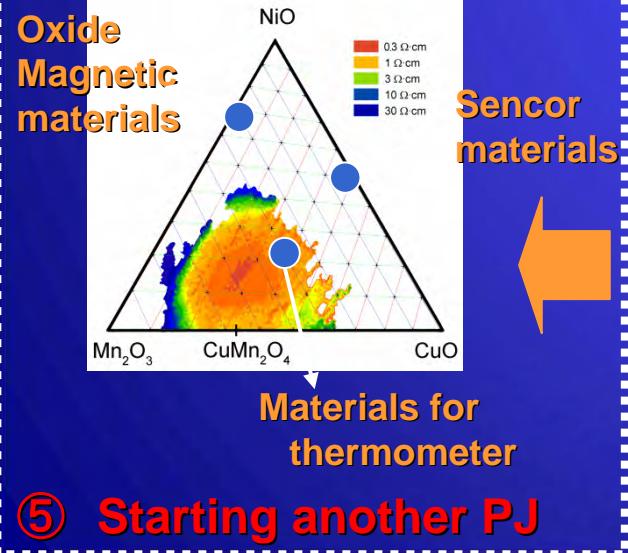
New materials discovery loop



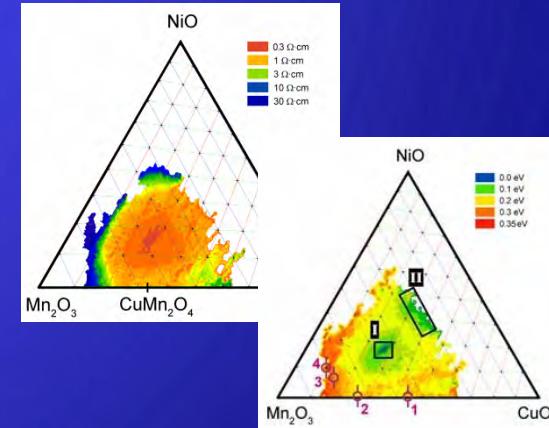
① Predict
(Calculation, Data base)

② Experiment design

③ Combi synthesis



④ Date handling in informatics



⑤ Starting another PJ

④ High throughput Characterization

Example 1 :Gate oxide

Requirement

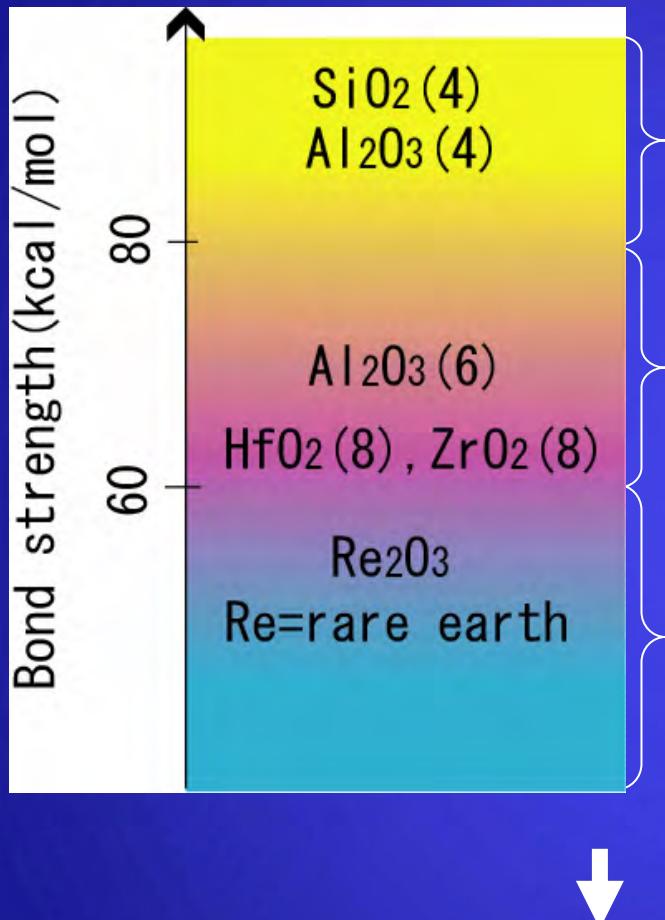
- 1) *Higher dielectric constant*
 - 2) *Amorphous structure*

Defect density in Oxide

(1) Ionicity : $\text{Y}_2\text{O}_3 < \text{HfO}_2, \text{MgO} < \text{TiO}_2, \text{Al}_2\text{O}_3 < \text{ZnO} < \text{SiO}_2$

(2) Valency : $\text{Y}:3+, \text{Hf}:4+, \text{Al}:3+ \quad \text{Zn}:2+$, simple
 $; \text{Ti } 3+, 4+$, mixed

Sun, Zachariasen Glass empirical rule

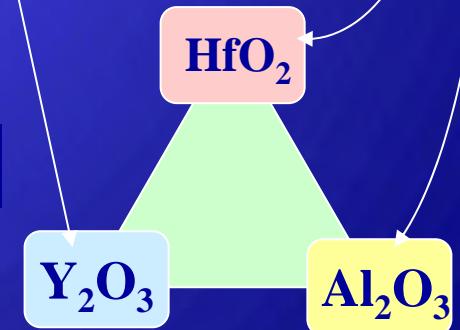


Network former
(Can be amorphous for itself)

Intermediate oxide
(Can be amorphous with
Network forming oxide and modifier)

Additional oxide
(can be amorphous
with network forming oxide)

Ternary alloying is inevitable for HfO₂ to be amorphous

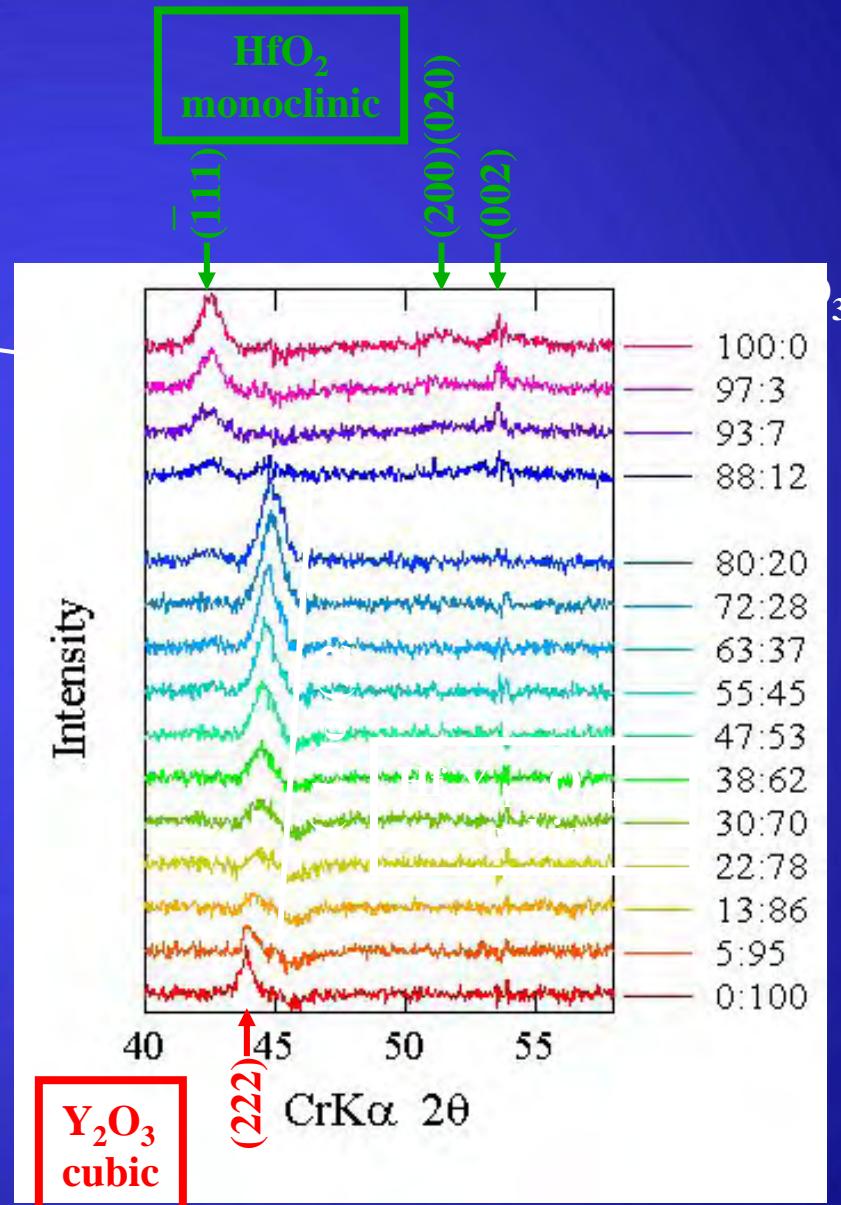


Combinatorial X-ray Diffraction System



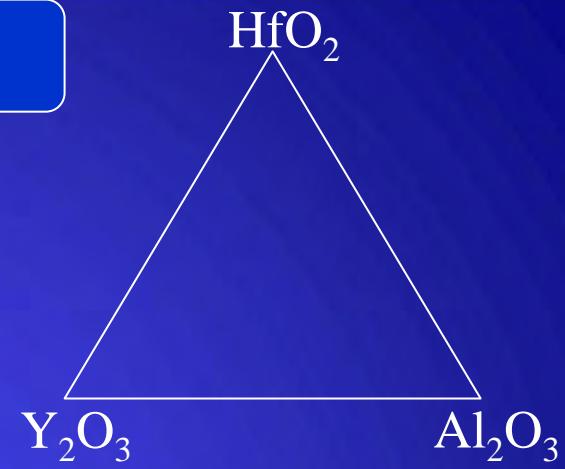
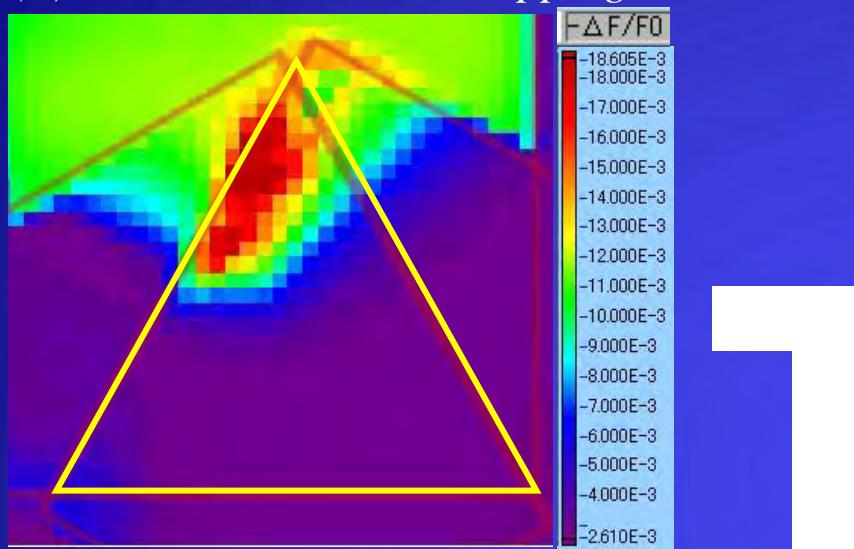
(Developed by
Dr.Watanabe
Dr.Fujimoto ,
AML-NIMS)

CombiXRD
(Bruker:
D8 Discovery
System)



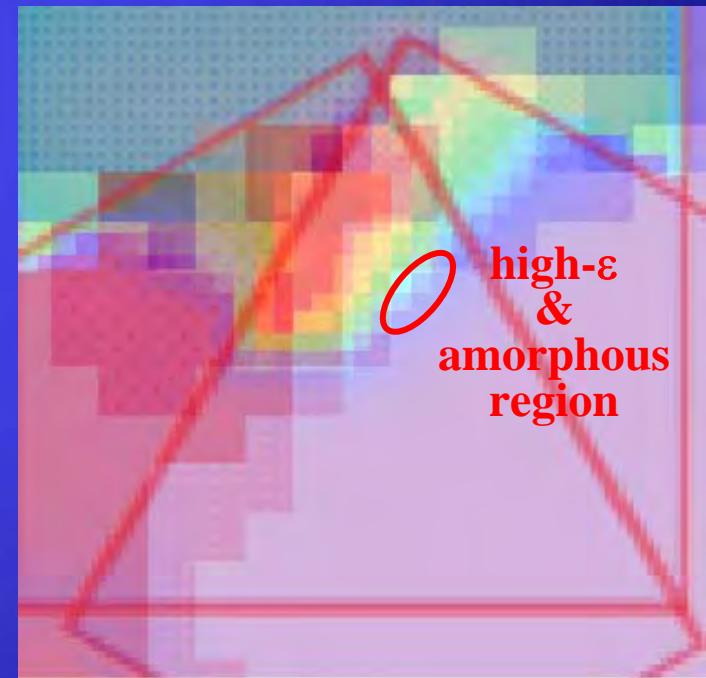
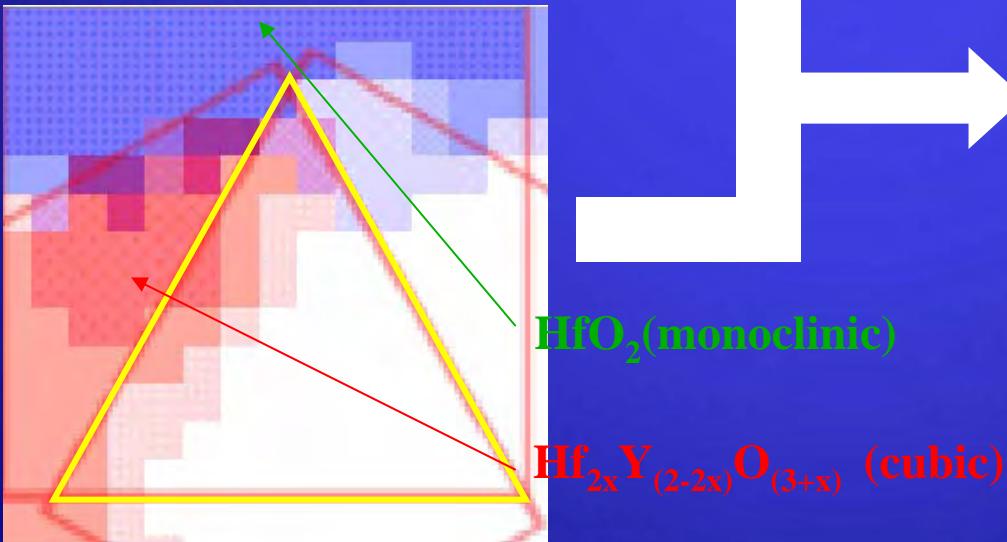
$\text{HfO}_2\text{-Y}_2\text{O}_3\text{-Al}_2\text{O}_3$ system

(1) Dielectric constant mapping

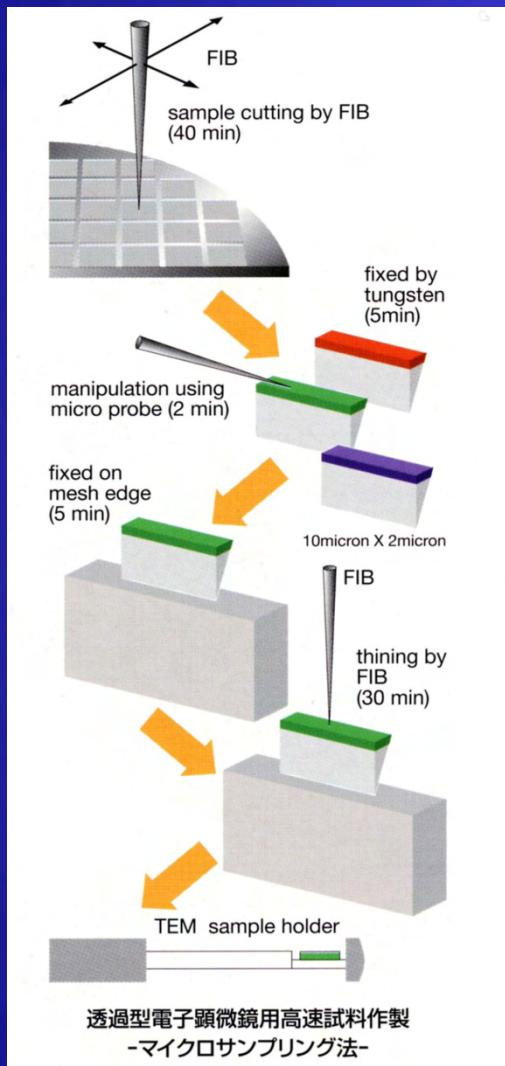


$T_{\text{sub}} = 300^\circ\text{C}$, laser power = $3\text{J}/\text{cm}^2$,
 $P_{\text{O}_2} = 1\text{e}^{-6}\text{Torr}$, post-annealed at 700°C

(2) Crystal structure mapping



Micro Structure Characterization for Combinatorial Samples



Micro Sampling Method

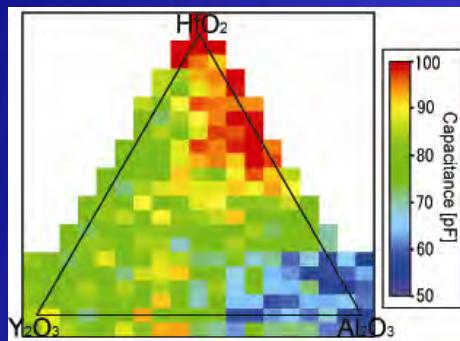


Hitachi FB-2000
+Micro sampling Unit

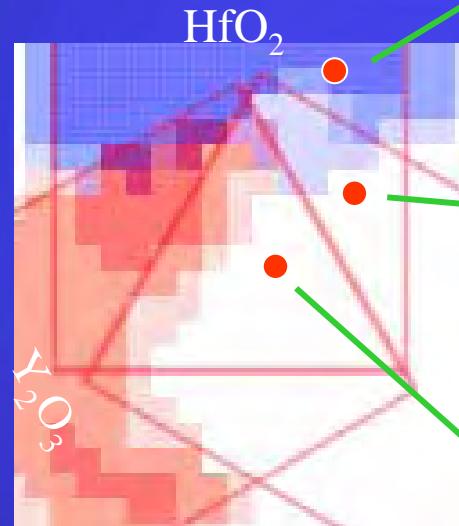


JEOL 4000EX (NIMS)
H-9000NAR (T.I.T)

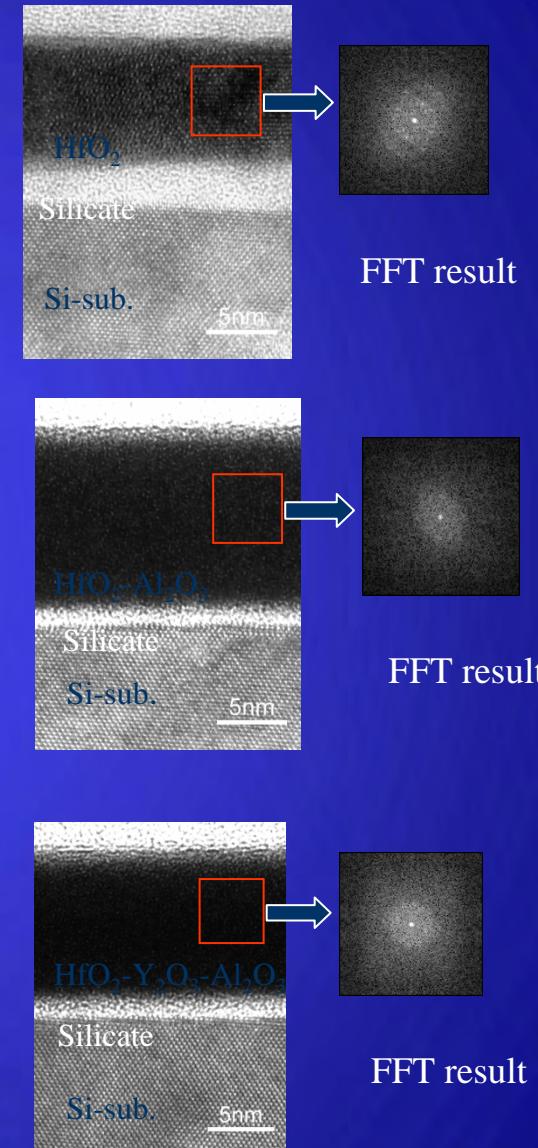
Characterizations of the combinatorial specimens



**Dielectric
Mapping
by C-V, I-V**



**Structure mapping
by Combinatorial XRD**



Interface structure mapping by TEM

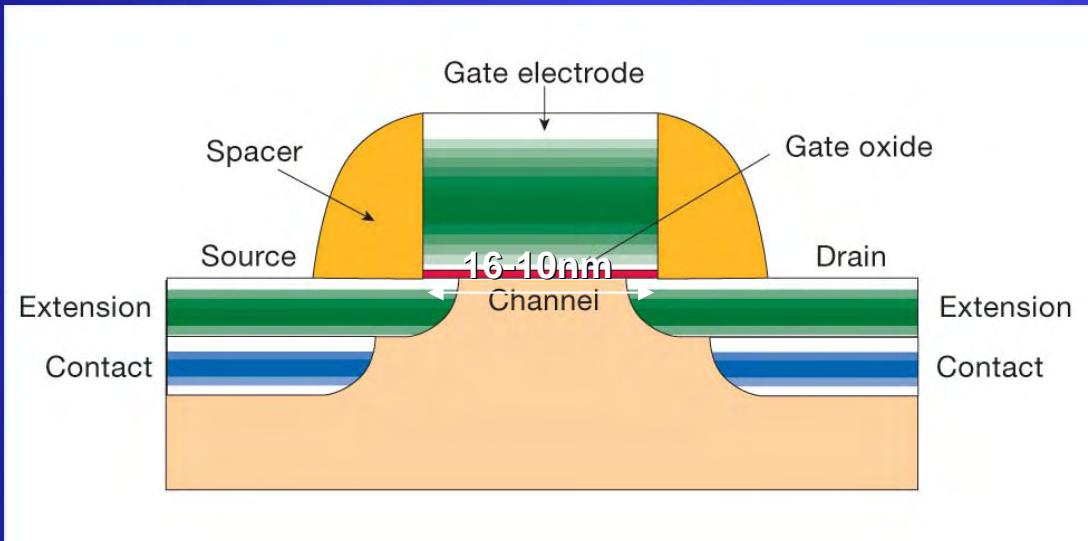
Example 2 : Metal Gate

Requirement

- 1) *Work function tuning*
- 2) *Interface control*
- 3) ***Amorphous structure***

Challenge of metal gate issues in hp32-22nm node

National Institute for Material Science



Poly metal gate



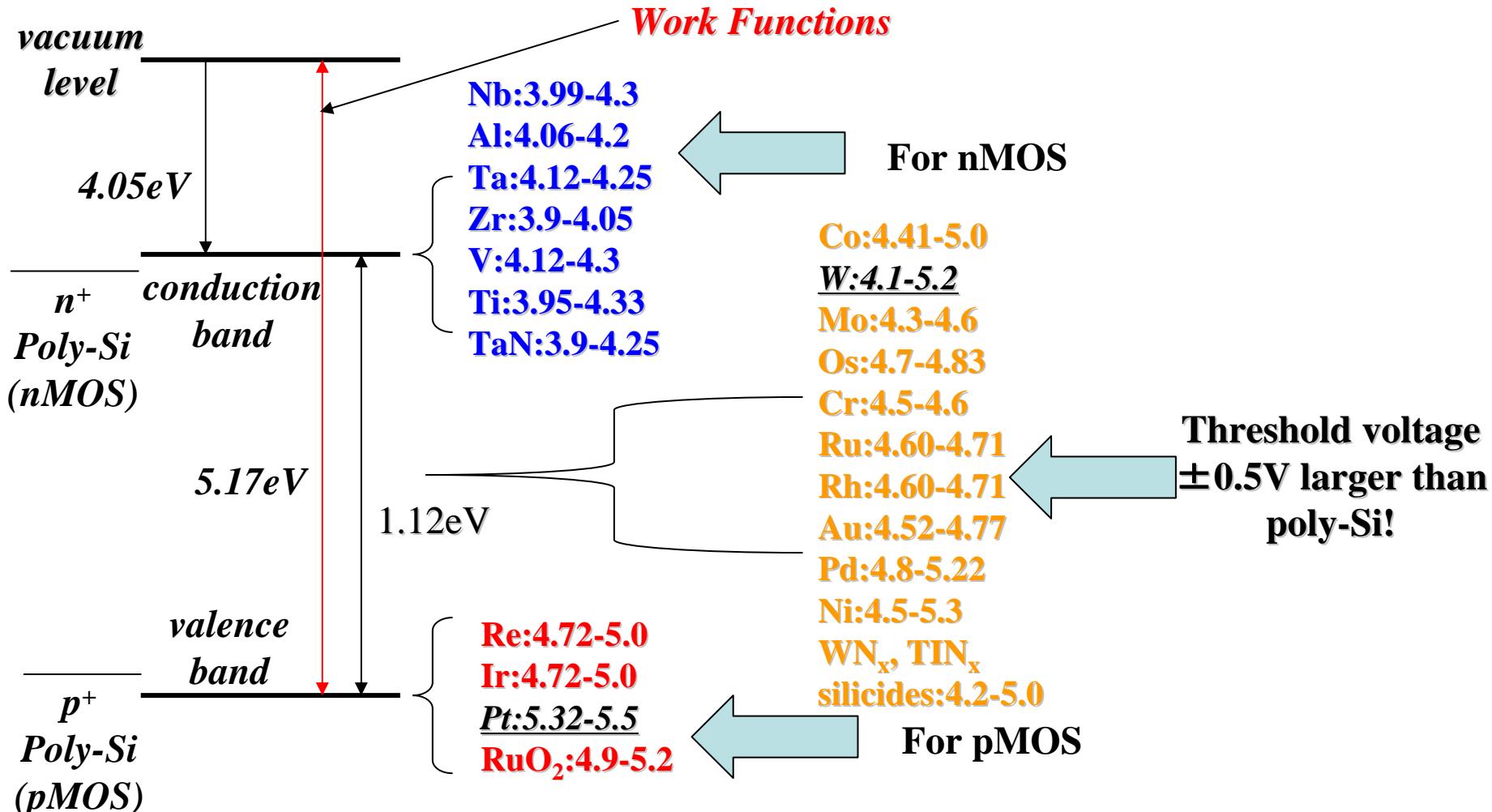
- 1) interface roughness
- 2) Work function
fractuation
- 3) Edge roughness



Metal glass gate



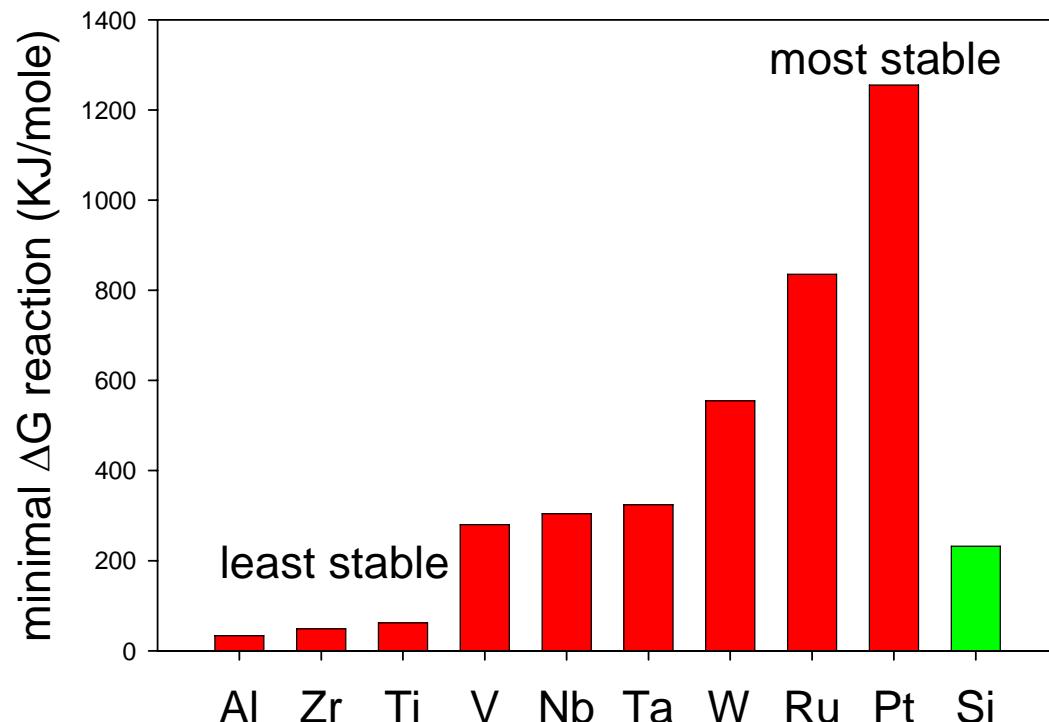
Work functions of various metals



(from Dr.Takagi Data)

Thermal reaction with HfO₂

Metal	Minimal Gibbs free energy of reaction (kJ/mole)
Al	33.42739
Zr	48.556
Ti	61.724
V	279.854
Nb	304.396
Ta	323.8836
W	554.42
Ru	835.599
Pt	1255.189
Si	231.836

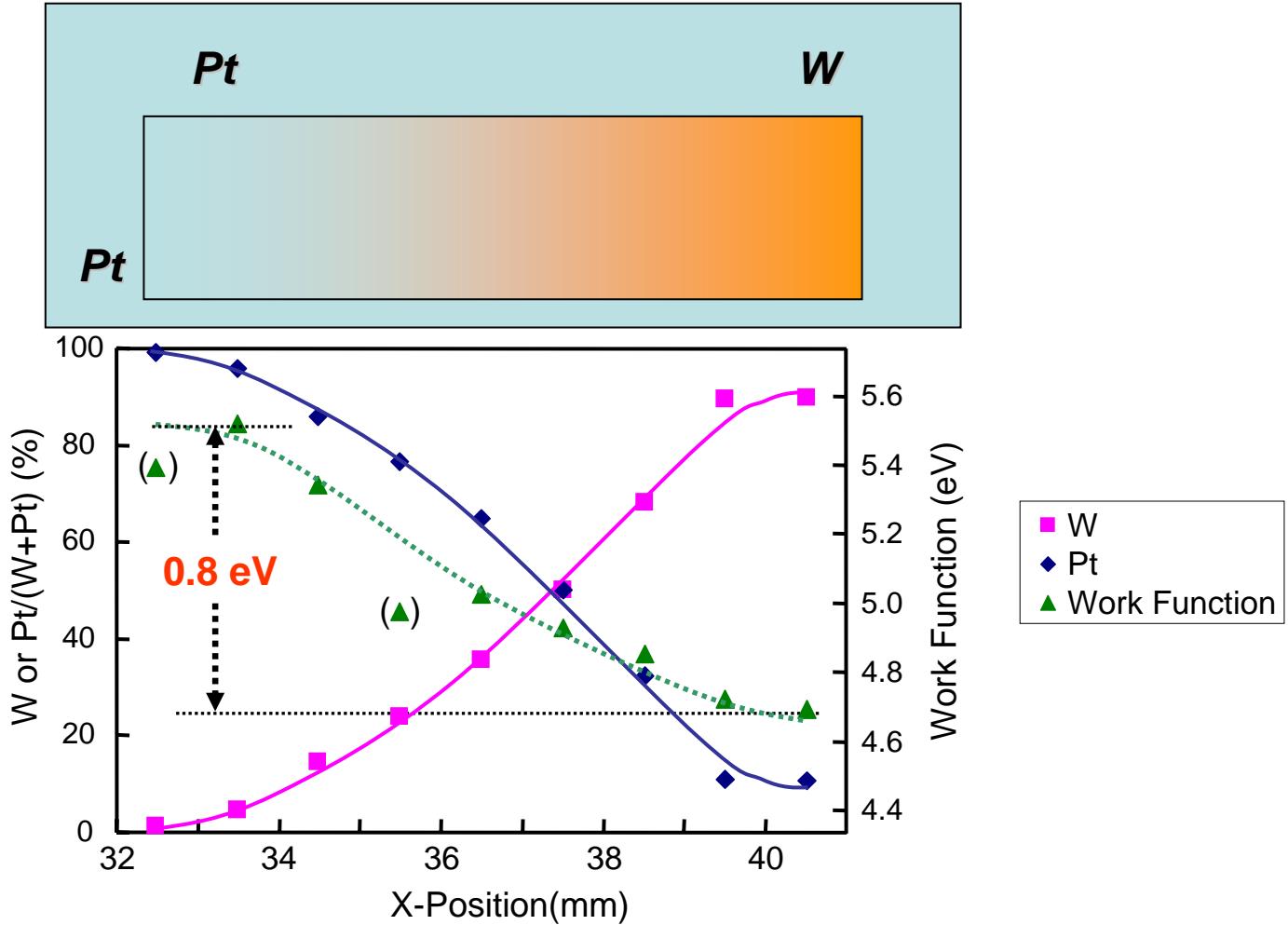


Work Function variation with composition spread film of Pt-W

High-k Net 次世代ゲート絶縁膜
研究ネットワーク



Thickness
:10 nm



Experimental

Pt-W alloy/p-Si

XPS

XRD

Composition
Work function
Crystal structure

HfO₂/SiO₂/p-Si, La₂O₃/p-Si

HfO₂: MOCVD
La₂O₃: E-beam evaporation

Deposition of Pt-W composition spread films with contact mask for capacitor electrodes

Forming gas annealing and/or oxidizing gas annealing

FGA (H₂/N₂, 5% H₂):
300-450°C
OGA (O₂/N₂, 0.1% O₂):
250-350°C

CV measurements

Electric properties

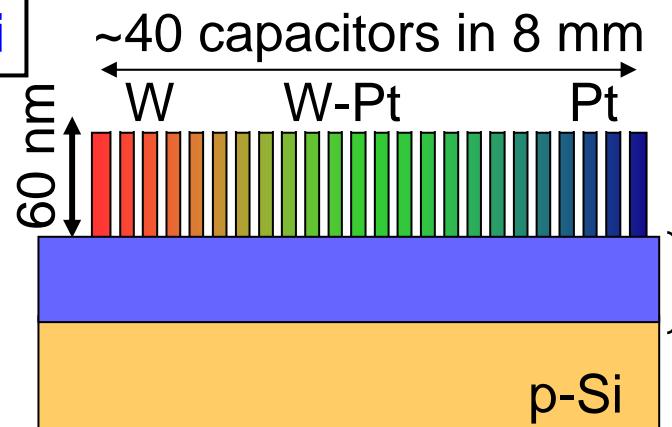
XTEM

Stacking structure

Pt-W (6nm)/HfO₂/SiO₂/p-Si

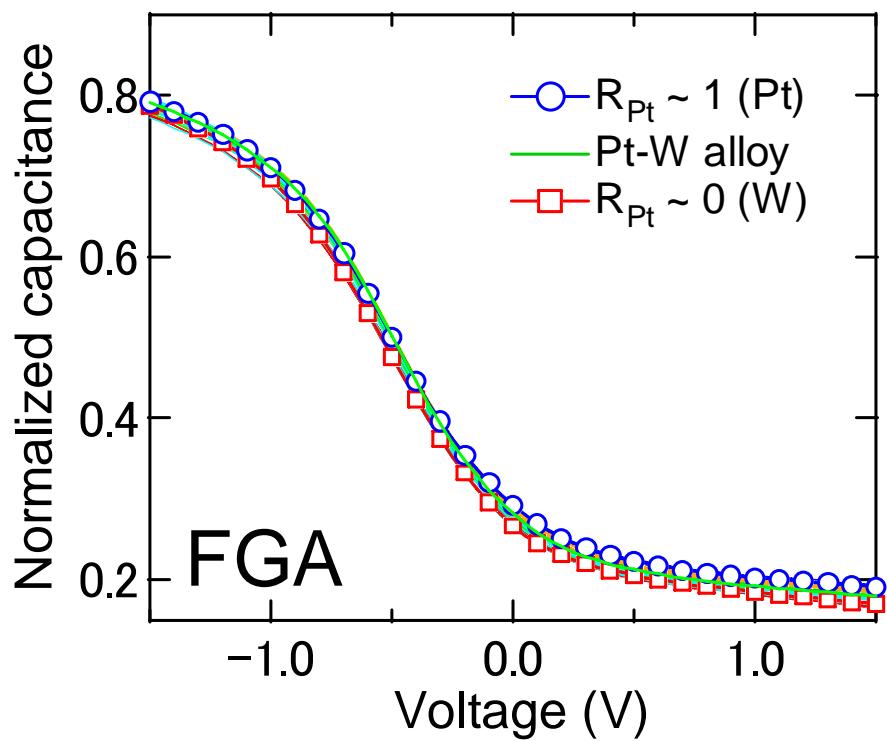
HXPES (6keV, SPring-8)

Composition
Depth profile

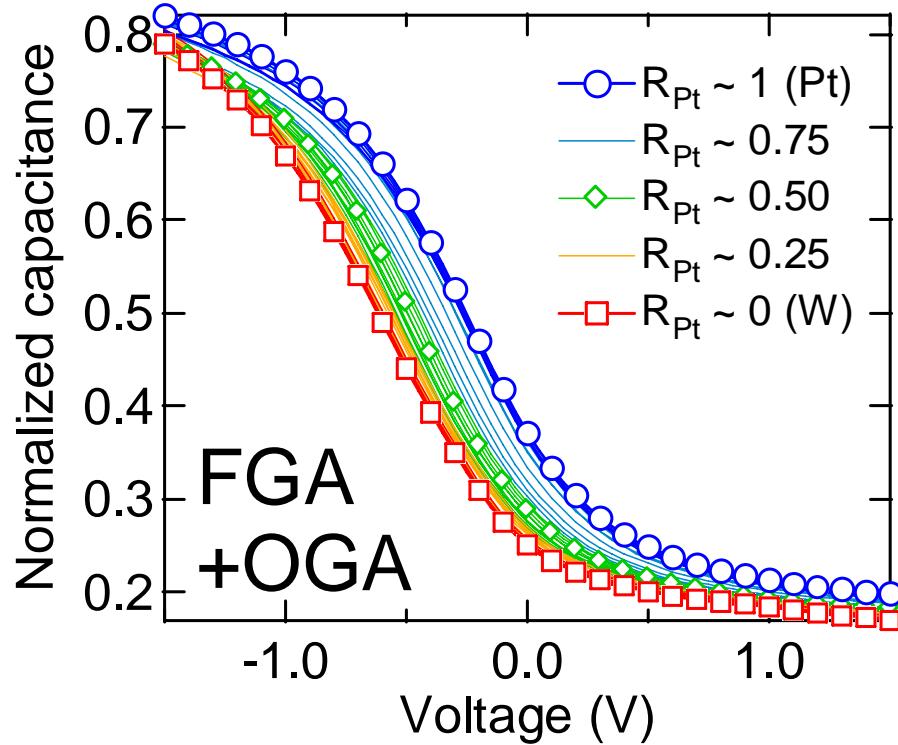


} ✓ HfO₂ (6 nm)/SiO₂
} ✓ La₂O₃ (7 nm)

Comparison of CV curves from HfO₂ films under different annealing conditions



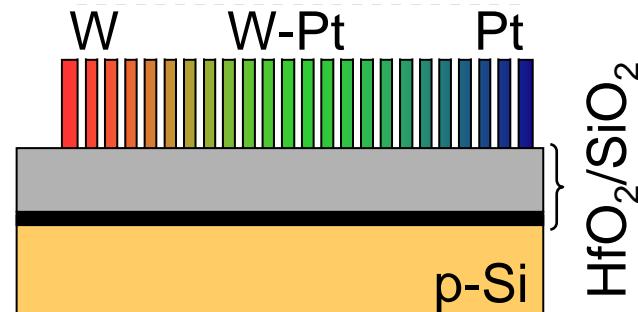
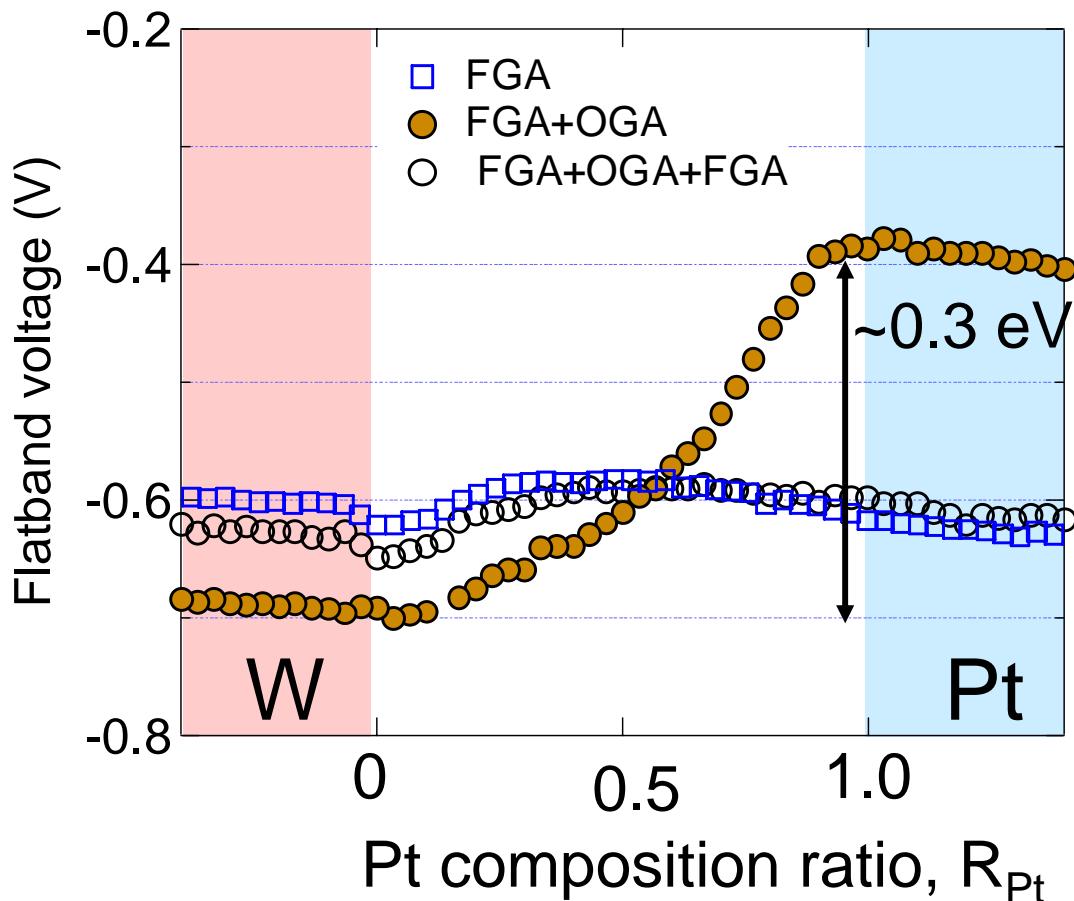
$$\Delta V_{fb} < 0.1 \text{ V}$$



$$\Delta V_{fb} = 0.3 \text{ V}$$

- Relative dielectric constant: $\varepsilon = 18.0$.

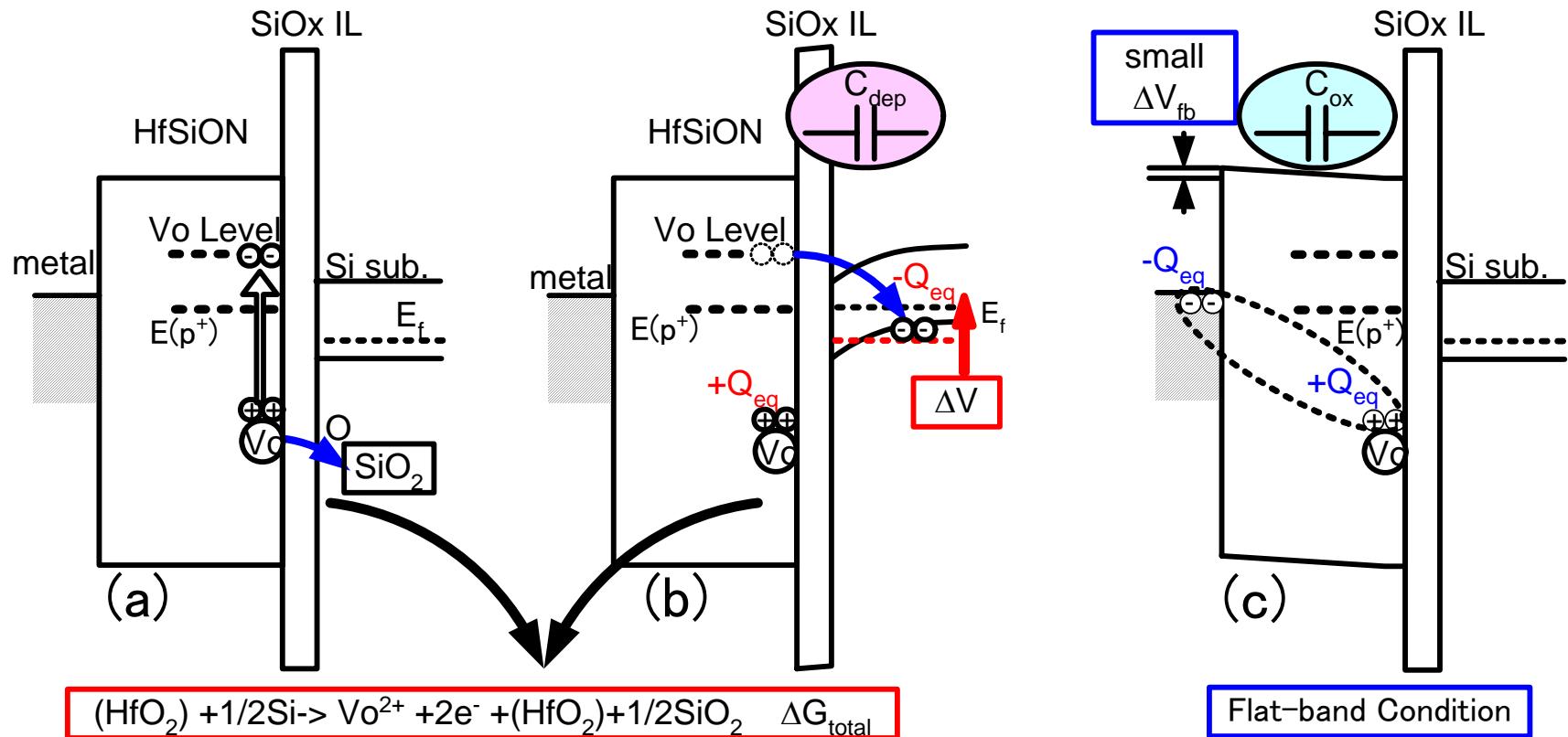
Changes in flatband voltage



FGA: 450°C, 30 min
OGA: 300°C, 30 min

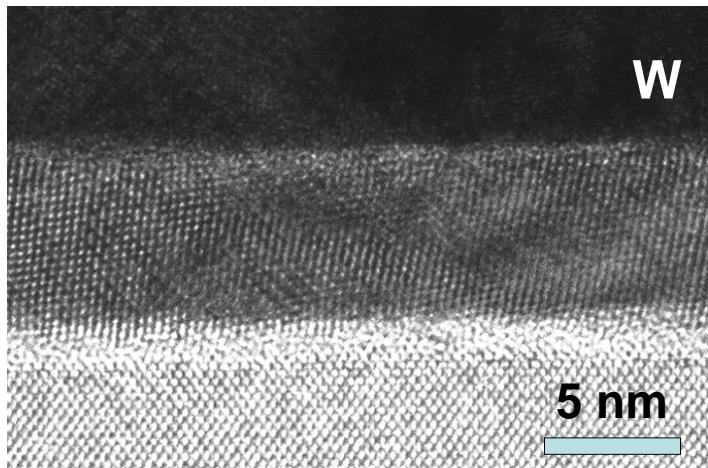
- The higher WF is, the larger the V_{fb} value after OGA becomes.
- The shift can be reversed by an additional second FGA.
- This observed phenomena is general and mainly depends on work function.

One interface affects another interface

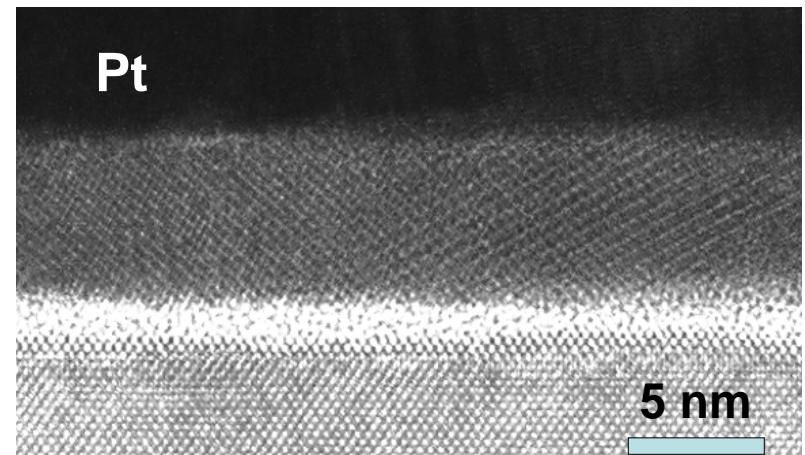


Cross-section TEM (HfO_2 , after FGA)

W/ HfO_2 / SiO_2 /Si

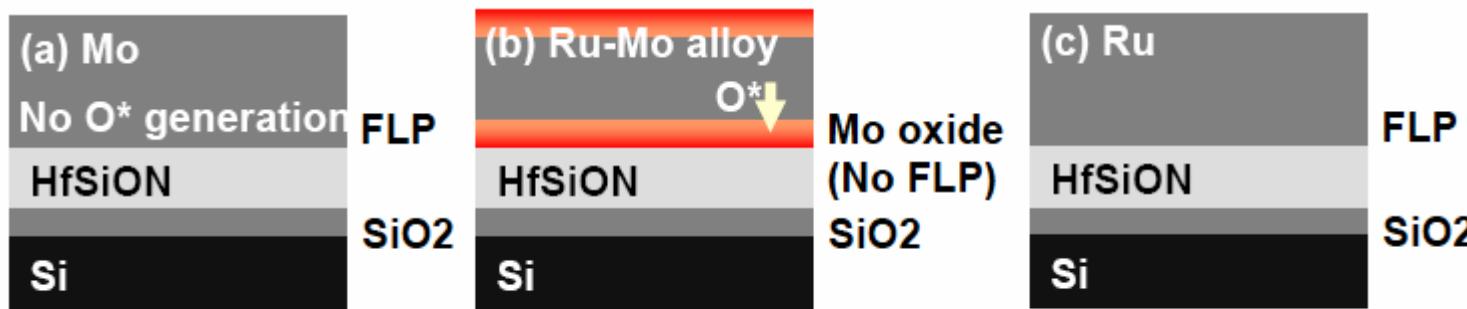
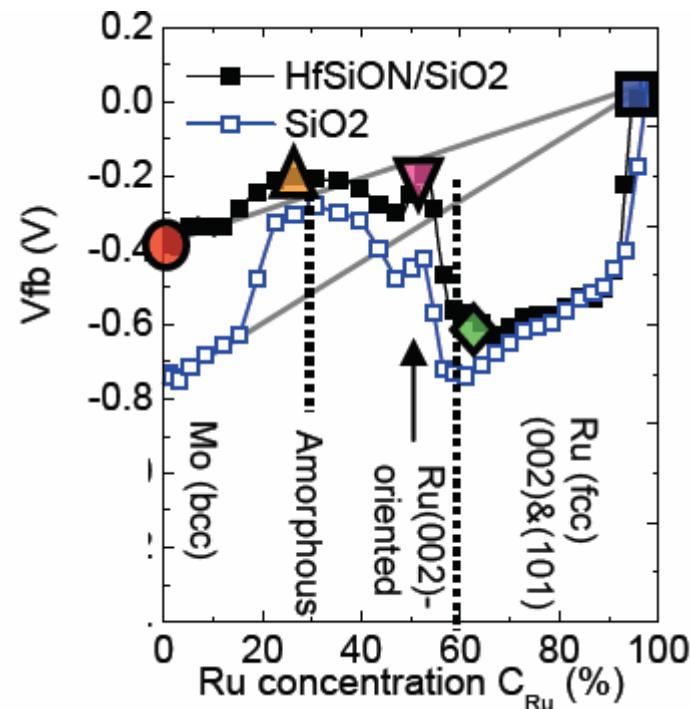
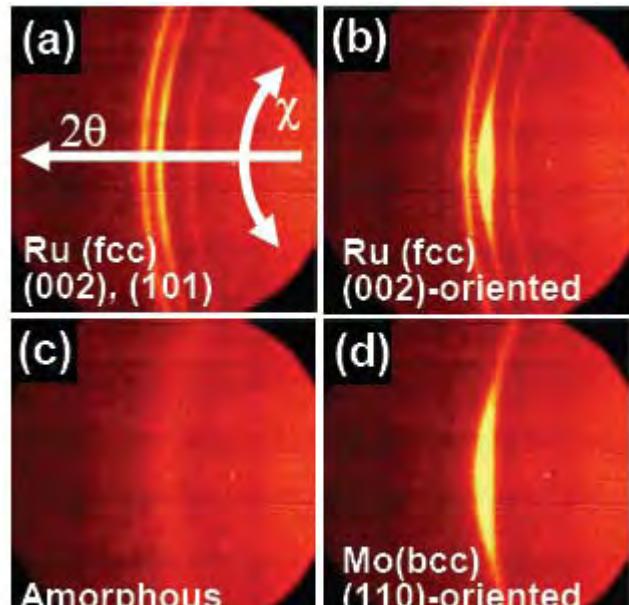


Pt/ HfO_2 / SiO_2 /Si



- (1) 6-nm-thick HfO_2 + 1-nm-thick interfacial SiO_2 layer
- (2) Crystallized
- (3) Grain size > film thickness
- (4) Bright portion (reaction layer?) at the metal/high-k interface.

Amorphous and phase separation

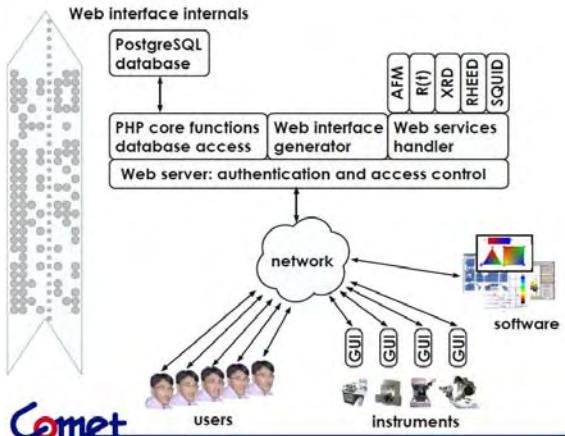


I
E
D M



Architecture of the materials informatiocs

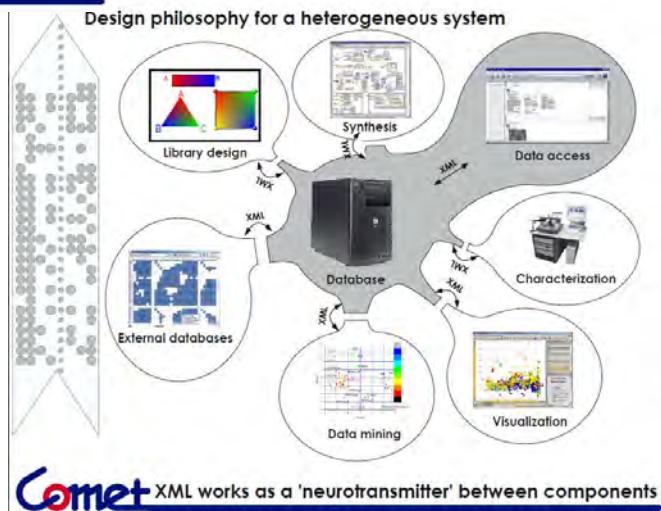
National Institute for Material Science



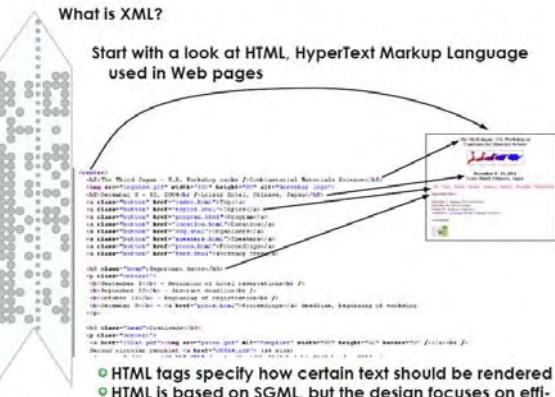
1) Group servers



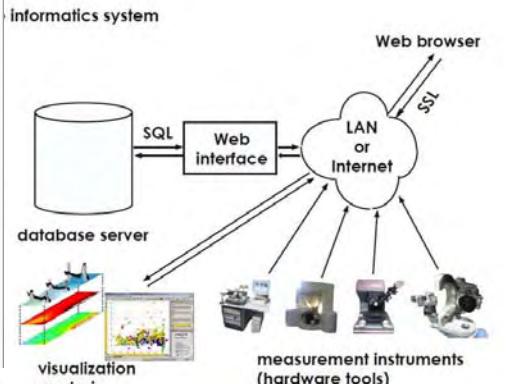
reference from other date



2) experiment design, conditions, results, measurement data are categorized by “MatLab”



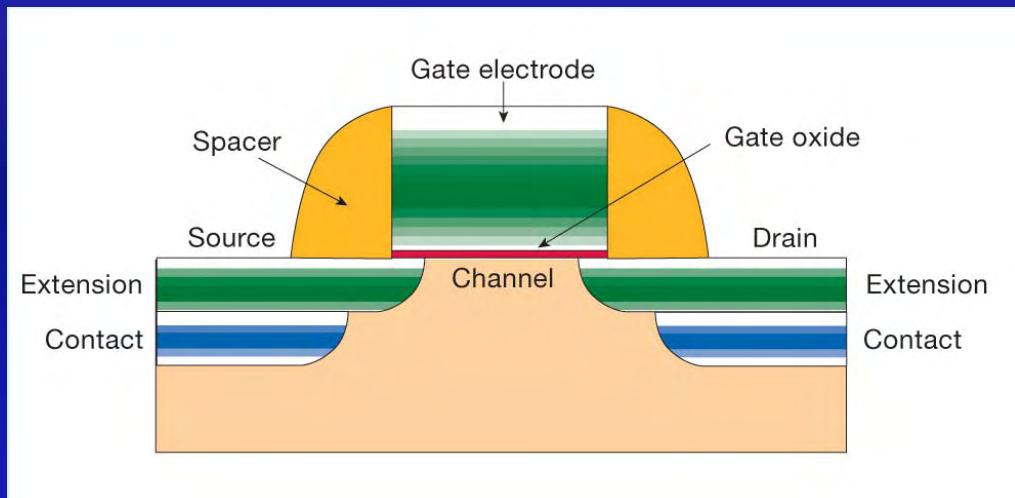
3) data sharing on website by XML data



4) domestic and international data sharing on web bases

Basic structure in Nano Electronics

National Institute for Material Science



1) Meta/Oxide Interface

**Nano CMOS
Spintronics (MIM)
Ohmic contact (GaN ,ZnO)**

2) Oxide/ Semiconductor Interface

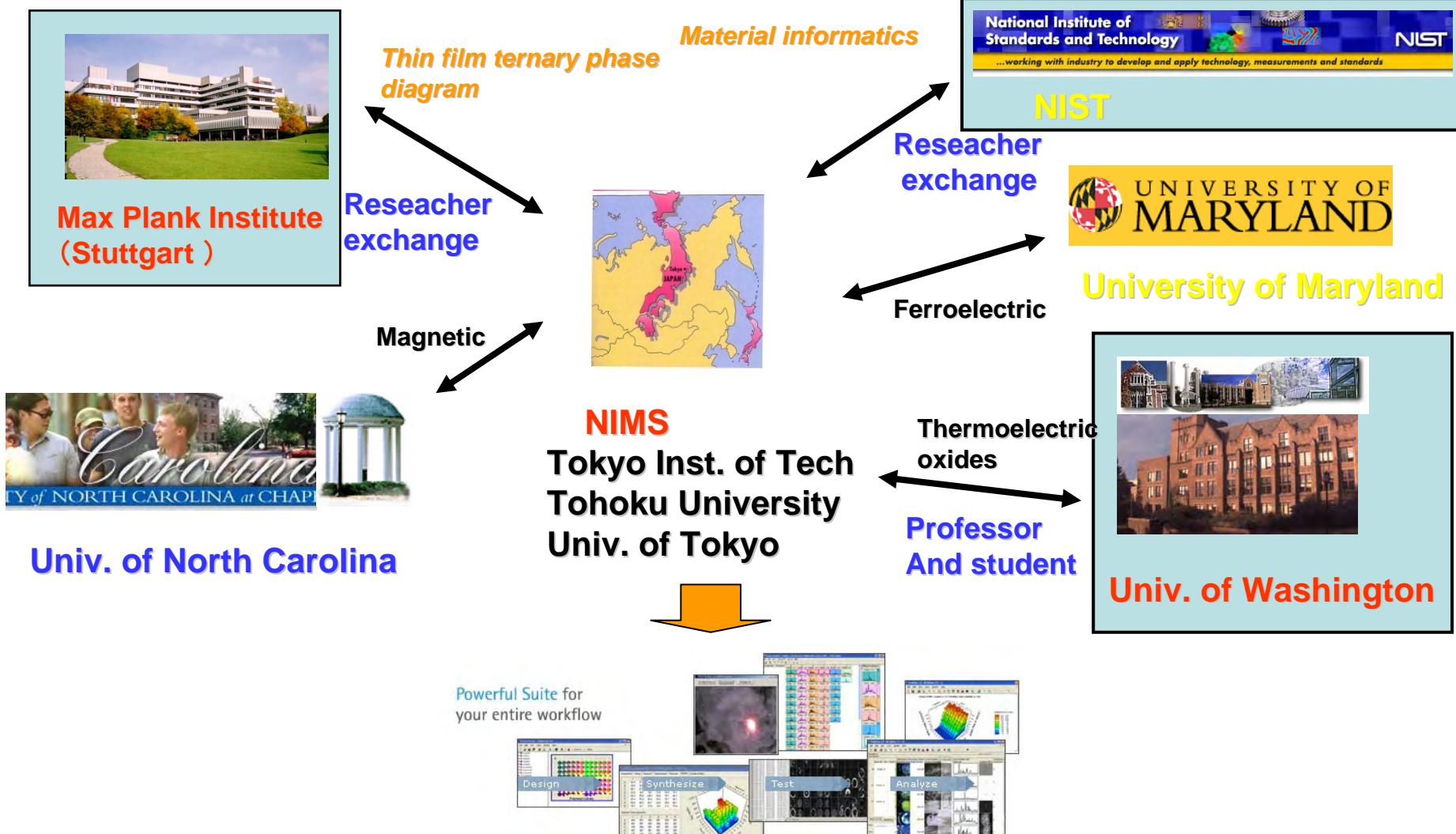
**Nano CMOS
Hetero Epitaxy on Si**

3) Metal /Oxide/ Semiconductor nano scale Interface

**Fermi level Pining
Solar Cells**

Hints from Photo catalysis

Informatics Network with NIMS



Material Informatics and its standardisation

High throughput nano materials exploration in nano electronics

- 1) Accelerating the nano materials exploration.**
- 2) Systematic materials data can be used for other research.**
- 3) Materials informatics which is shared with researchers or community can provide a lot of seeds for future innovation.**