

Research Interests

PARTICIPANTS' AREAS OF RESEARCH AND TECHNICAL WORK

*	Committee Member
**	Speaker

Gilda Garreton, Sun Microsystems Research Labs



My main research focuses on VLSI CAD algorithms including Java multi-threaded tools. I received my Ph.D. from the Swiss Institute of Technology (ETHZ) in Zürich and my thesis was on 2D/3D mesh generation tools suitable for device simulation.