

US – JAPAN WORKSHOP NEW-GENERATION COMPUTERS: QUANTUM ANNEALING AND COHERENT COMPUTING

- Quantum annealing and coherent Ising, XY, and Heisenberg machines
- Combinatorial optimization problems and IoT
- Semi-definite programming, simulated annealing and other heuristics
- Implementation and performance evaluation in optical, superconducting, and semiconducting devices.

December 10-11, 2015 Stanford University

Organized by Yoshihisa Yamamoto Quantum Artificial Brain ImPACT Program, The Cabinet Office of Japan & Stanford University Will Oliver Quantum Enhanced Optimization IARPA Program, MIT Lincoln Laboratory

> Workshop Secretariat (JST) email: <u>impact-ymm@jst.go.jp</u> Local contact (Stanford): <u>yurikap@stanford.edu</u>, <u>rsasaki@stanford.edu</u>

PROGRAM

December 10 (Thu)					
8:00 - 9:00		Registration			
9:00 - 9:10	(10)	Welcome Remarks (Yoshihisa Yamamo	to, Will Oliver)		
9:10 - 9:40	(30)	Yoshihisa Yamamoto	Coherent Ising machine		
		(ImPACT Program/Cabinet Office			
		of Japan, Stanford University)			
9:40 - 10:20	(40)	Alireza Marandi (Stanford University)	OPO network as CIM		
10:20 - 10:50		Coffee Break			
10:50 - 11:30	(40)	Will Oliver (MIT / MIT Lincoln Laboratory)	Superconducting technologies for quantum enhanced optimization		
11:30 - 12:10	(40)	Simon Gustavsson (MIT)	Capacitively shunted flux qubit		
12:10 - 14:00			Free Time		
14:00 - 14:40	(40)	Shoko Utsunomiya (National Institute of Informatics)	Laser network as coherent XY machine		
14:40 - 15:20	(40)	Hiroki Takesue (NTT)	Large-scale CIM		
15:20 - 15:50			Coffee Break		
15:50 - 16:30	(40)	David Hover (MIT Lincoln Laboratory)	Josephson traveling wave parametric amplifier		
16:30 - 17:10	(40)	Daniel Lidar (University of Southern California)	Error correction for quantum annealing		
17:30 - 19:00		Poster Session			
December 11 (Fri)					
9:00 - 9:40	(40)	Matthias Troyer (ETH Zurich)	Classical, quantum, and quantum-inspired annealing		
9:40 - 10:20	(40)	Eleanor Rieffel (NASA-Ames Research Center)	Programming quantum annealers, application case studies, and analyses of enhancement mechanisms		
10:20 - 10:50		Coffee Break			
10:50 - 11:30	(40)	Yuichi Katori (Future University Hakodate)	Associative memory with CIM		
11:30 - 12:10	(40)	Timothee Leleu (The University of Tokyo)	Combinatorial optimization using dynamical phase transition in driven-dissipative systems		
12:10 - 14:00		Free Time			
14:00 - 14:40	(40)	Vadim Smelyanskiy (Google)	Instantons in quantum annealing: thermally assisted tunneling vs quantum Monte Carlo simulations		
14:00 - 14:40 14:40 - 15:20	(40)				
		(Google) Sergio Boixo	quantum Monte Carlo simulations		
14:40 - 15:20		(Google) Sergio Boixo	quantum Monte Carlo simulations Tunneling in physical quantum annealing		
14:40 - 15:20 15:20 - 15:50	(40)	(Google) Sergio Boixo (Google) Hidetoshi Nishimori	quantum Monte Carlo simulations Tunneling in physical quantum annealing Coffee Break Bayesian inference of the Ising model ground state out of noisy		

POSTER PRESENTATION

December 10 (Thursday) 17:30pm - 19:00pm					
	NAME	AFFILIATION	TITLE		
1	Ryan Hamerly	Stanford University	Modeling domain formation in large-scale Ising machines		
2	Yoshitaka Haribara	National Institiute of Informatics The University of Tokyo	A coherent Ising machine for MAX-CUT problems against simulated annealing and semi-definite programming		
3	Takahiro Inagaki	NTT	Time-division-multiplexed optical parametric oscillator for large-scale coherent Ising machine		
4	Peter McMahon	Stanford University	An OPO Ising machine using measurement feedback		
5	Hiromasa Sakaguchi	National Institiute of Informatics The University of Tokyo	Community detection by using laser network		
6	Shuhei Tamate	National Institiute of Informatics	Analog simulation of classical XY models with a mode-locked fiber laser		
7	Simon Gustavsson	МІТ	Quantum computation with superconducting circuits		
8	David Hover	MIT Lincoln Laboratory	High-fidelity quantum informationi processing with superconducting circuits		

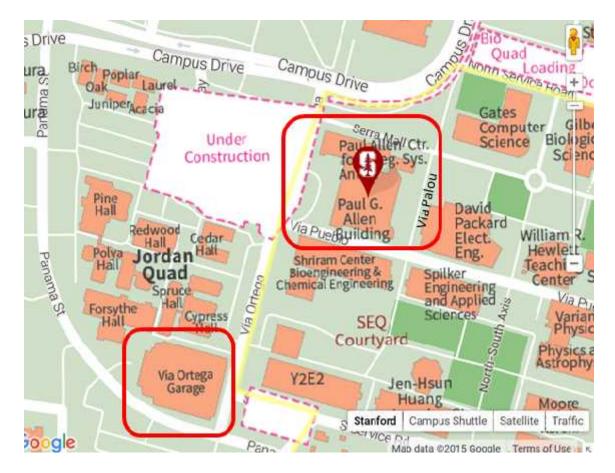
VENUE & PARKING

CIS-X Auditorium Paul G. Allen Building (also known as Center for Integrated Systems (CIS)) 420 Via Palou Mall

Stanford, CA 94305-4070

https://campus-map.stanford.edu/index.cfm?ID=04-050





The nearest visitor parking (Via Ortega Garage) is located at the corner of Via Ortega and Panama Street. There is no free parking on campus from 6am to 4pm. The visitor "A" permits are available at the registration desk in front of CIS-X Auditorium from 8am to 4pm on December 10 and 11. The visitor will scratch off the date and it has to be hanged from a rear-view mirror, facing out. The "A" permit is valid for one-full day for both "A" and "C" zones but not for "P"(pay) parking.

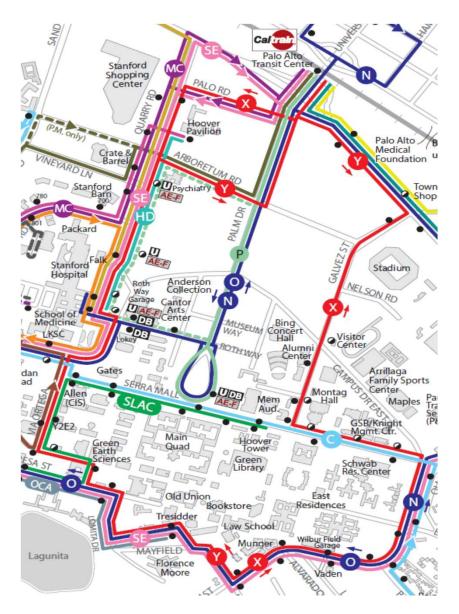
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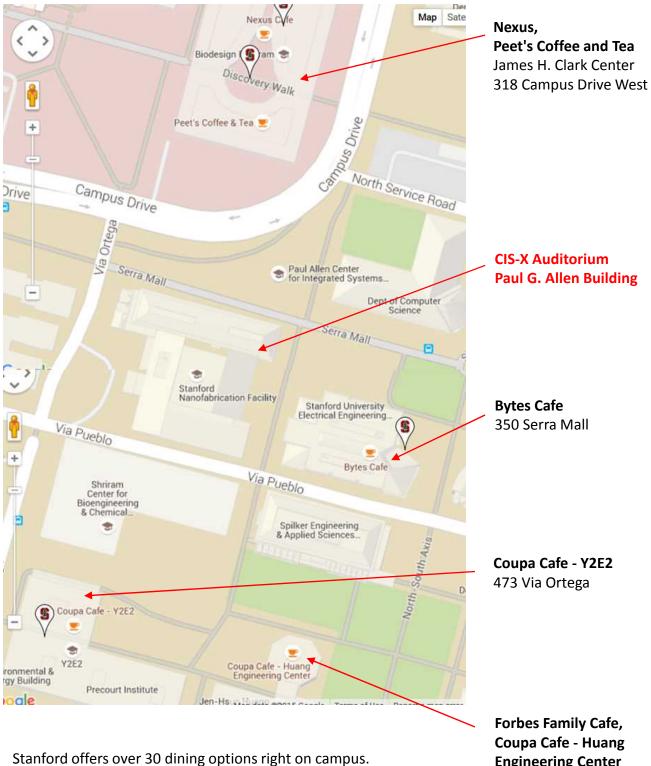
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Line Y (Clockwise) Via Ortega @ Y2E2 \rightarrow Palo Alto Transit Center (Caltrain platform) <u>http://transportation.stanford.edu/marguerite/y/</u>



Larger map & other routes http://transportation.stanford.edu/marguerite/schedules.php

DINING MAP



For more options, visit this link http://visit.stanford.edu/activities/dining.html **Engineering Center** 475 Via Ortega

TAXI AND AIRPORT SHUTTLE

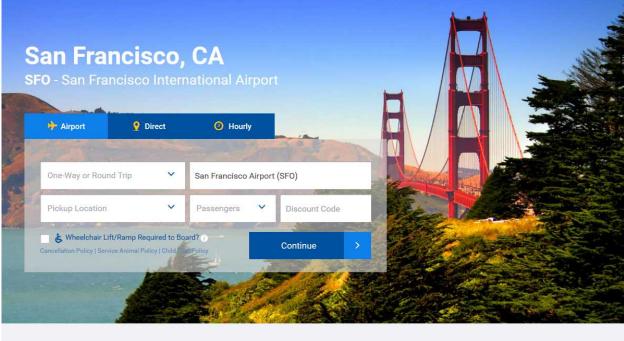
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